Technical Specification

PKM 7000A series Direct Converters Input 66-160 V, Output up to 12.5 A / 150 W	28701-BMR 711 Rev. B October 2017

Key Features

- Industry standard case dimensions 57.91 x 36.8 x 12.7 mm (2.28 x 1.45 x 0.5 in)
- High efficiency, typ. 89% at 24 Vout Full load
- 3000 Vdc input to output isolation
- Meets requirements according to IEC/EN/UL 60950-1
- MTBF 4 Mh
- Compliant to EN50155

General Characteristics

- Output over voltage protection
- Input under voltage shutdown
- Over temperature protection
- Monotonic startup
- Output short-circuit protection
- Remote control
- Output voltage adjust function
- ISO 9001/14001 certified supplier

Safety Approvals

c UL us

Design for Environment

Pb RoHS compatible

Meets requirements in high-temperature lead-free soldering processes.

Contents

Ordering Information

Product program	Output
PKM 7111A PIP	5V, 20A / 100 W
PKM 7113A PIP	12V, 8.3A / 100 W
PKM 7115A PIP	15V, 6.67A / 100 W
PKM 7116ZA PIP	24V, 4.16A / 100 W
PKM 7116JA PIP	48V, 2.08A/ 100W
PKM 7213A PIP	12V, 12.5A / 150 W
PKM 7215A PIP	15V, 10A / 150 W
PKM 7216ZA PIP	24V, 6.25A / 150 W

Product number and Packaging

PKM7XXXX $n_1n_2n_3$			
Options	n_1	n_2	n_3
Mounting	o		
Remote Control logic		o	
Baseplate			o

Options	Description	
n_1	PI	Through hole
n_2	 P	Negative Positive
n_3	 LHS LHS HS	No heat sink 10 mm 1/4'' heat sink(100W) 20 mm 1/2'' heat sink(150W) 20 mm 1/4'' heat sink

Example a 150W through-hole mounted, positive logic, nominal pin length Product with 20mm 1/2'' baseplate would be PKM7216ZAPIPHS

General Information

Reliability

The failure rate (λ) and mean time between failures (MTBF= $1/\lambda$) is calculated at max output power and an operating ambient temperature (T_A) of +25°C. Flex uses Telcordia SR-332 Issue 3 Method 1 to calculate the mean steady-state failure rate and standard deviation (σ).

Telcordia SR-332 Issue 3 also provides techniques to estimate the upper confidence levels of failure rates based on the mean and standard deviation.

Mean steady-state failure rate, λ	Std.deviation, σ
237 nFailures/h	107 nFailures/h

MTBF (mean value) for the PKM XXX series = 4 Mh.
MTBF at 90% confidence level = 3.6 Mh

Compatibility with RoHS requirements

The products are compatible with the relevant clauses and requirements of the RoHS directive 2011/65/EU and have a maximum concentration value of 0.1% by weight in homogeneous materials for lead, mercury, hexavalent chromium, PBB and PBDE and of 0.01% by weight in homogeneous materials for cadmium.

Exemptions in the RoHS directive utilized in Flex products are found in the Statement of Compliance document.

Flex fulfills and will continuously fulfill all its obligations under regulation (EC) No 1907/2006 concerning the registration, evaluation, authorization and restriction of chemicals (REACH) as they enter into force and is through product materials declarations preparing for the obligations to communicate information on substances in the products.

Quality Statement

The products are designed and manufactured in an industrial environment where quality systems and methods like ISO 9000, Six Sigma, and SPC are intensively in use to boost the continuous improvements strategy. Infant mortality or early failures in the products are screened out and they are subjected to an ATE-based final test. Conservative design rules, design reviews and product qualifications, plus the high competence of an engaged work force, contribute to the high quality of the products.

Warranty

Warranty period and conditions are defined in Flex General Terms and Conditions of Sale.

Limitation of Liability

Flex does not make any other warranties, expressed or implied including any warranty of merchantability or fitness for a particular purpose (including, but not limited to, use in life support applications, where malfunctions of product can cause injury to a person's health or life).

Safety Specification

General information

Flex DC/DC converters and DC/DC regulators are designed in accordance with the safety standards IEC 60950-1, EN 60950-1 and UL 60950-1 *Safety of Information Technology Equipment*.

IEC/EN/UL 60950-1 contains requirements to prevent injury or damage due to the following hazards:

- Electrical shock
- Energy hazards
- Fire
- Mechanical and heat hazards
- Radiation hazards
- Chemical hazards

On-board DC/DC converters and DC/DC regulators are defined as component power supplies. As components they cannot fully comply with the provisions of any safety requirements without "conditions of acceptability". Clearance between conductors and between conductive parts of the component power supply and conductors on the board in the final product must meet the applicable safety requirements. Certain conditions of acceptability apply for component power supplies with limited stand-off (see Mechanical Information and Safety Certificate for further information). It is the responsibility of the installer to ensure that the final product housing these components complies with the requirements of all applicable safety standards and regulations for the final product.

Component power supplies for general use should comply with the requirements in IEC/EN/UL 60950-1 *Safety of Information Technology Equipment.* Product related standards, e.g. IEEE 802.3af *Power over Ethernet*, and ETS-300132-2 *Power interface at the input to telecom equipment, operated by direct current (dc)* are based on IEC/EN/UL 60950-1 with regards to safety.

Flex DC/DC converters, Power interface modules and DC/DC regulators are UL 60950-1 recognized and certified in accordance with EN 60950-1. The flammability rating for all construction parts of the products meet requirements for V-0 class material according to IEC 60695-11-10, *Fire hazard testing, test flames* – 50 W horizontal and vertical flame test methods.

Isolated DC/DC converters & Power interface modules

The product may provide basic or functional insulation between input and output according to IEC/EN/UL 60950-1 (see Safety Certificate), different conditions shall be met if the output of a basic or a functional insulated product shall be considered as safety extra low voltage (SELV).

For basic insulated products (see Safety Certificate) the output is considered as safety extra low voltage (SELV) if one of the following conditions is met:

- The input source provides supplementary or double or reinforced insulation from the AC mains according to IEC/EN/UL 60950-1.
- The input source provides functional or basic insulation from the AC mains and the product's output is reliably connected to protective earth according to IEC/EN/UL 60950-1.

For functional insulated products (see Safety Certificate) the output is considered as safety extra low voltage (SELV) if one of the following conditions is met:

- The input source provides double or reinforced insulation from the AC mains according to IEC/EN/UL 60950-1.
- The input source provides basic or supplementary insulation from the AC mains and the product's output is reliably connected to protective earth according to IEC/EN/UL 60950-1.
- The input source is reliably connected to protective earth and provides basic or supplementary insulation according to IEC/EN/UL 60950-1 and the maximum input source voltage is 60 Vdc.

Galvanic isolation between input and output is verified in an electric strength test and the isolation voltage (V_{iso}) meets the voltage strength requirement for basic insulation according to IEC/EN/UL 60950-1.

It is recommended to use a slow blow fuse at the input of each DC/DC converter. If an input filter is used in the circuit the fuse should be placed in front of the input filter. In the rare event of a component problem that imposes a short circuit on the input source, this fuse will provide the following functions:

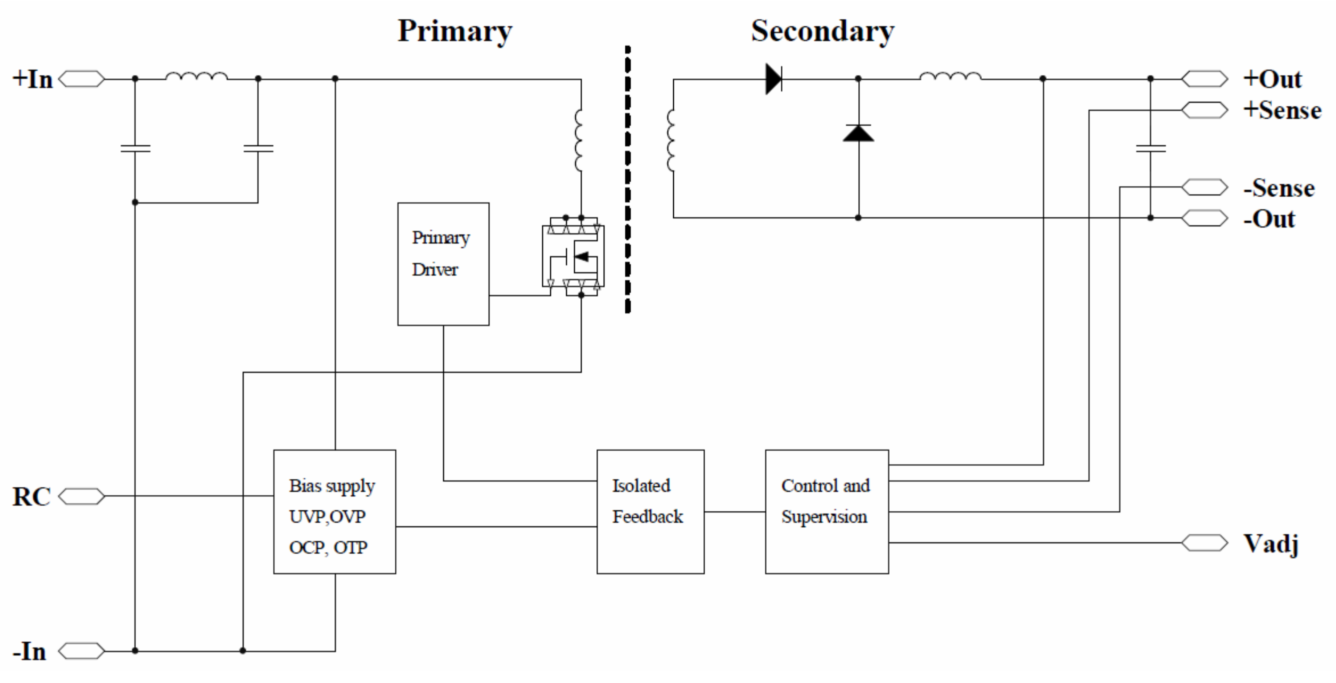
- Isolate the fault from the input power source so as not to affect the operation of other parts of the system
- Protect the distribution wiring from excessive current and power loss thus preventing hazardous overheating

Absolute Maximum Ratings

Characteristics			min	typ	max	Unit
T_{P1}	Operating Temperature (see Thermal Consideration section)		-40		+115	°C
T_S	Storage temperature		-55		+125	°C
V_I	Input voltage		0		200	V
C_{out}	Output capacitance		0		5000	µF
V_{iso}	Isolation voltage (input to output)				3000	Vdc
V_{iso}	Isolation voltage (input to baseplate)				2000	Vdc
V_{iso}	Isolation voltage (baseplate to output)				1000	Vdc
V_{tr}	Input voltage transient {according to ETSI EN 300 132-2 and Telcordia GR-1089-CORE}				200	V
V_{adj}	Adjust pin voltage (see Operating Information section)		0		$1.15xV_o$	V
V_{RC}	Remote Control pin voltage (see Operating Information section)	Positive logic option	0		8	V
		Negative logic option	0		8	V

Stresses above those listed under Absolute Maximum Ratings may cause permanent damage to the device. This is a stress rating only; functional operation of the device at these or any other conditions above those indicated in the Electrical Specification section of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Fundamental Circuit Diagram



Electrical Specification

5 V, 20 A / 100 W

PKM 7111A PIP

TP1 = -40 to 105ºC, VI = 66 to 160 V, {sense pins connected to output pins} unless otherwise specified under Conditions.
Typical values given at: TP1 = +25°C, VI= 110 VI max IO, unless otherwise specified under Conditions.
Additional Cin = 47 µF, Cout = 10uF ceramic Cap. + 22uF E-Cap. See Operating Information section for selection of capacitor types.

Characteristics		Conditions	min	typ	max	Unit
V_I	Input voltage range		66		160	V
V_{Ioff}	Turn-off input voltage	Decreasing input voltage	60	62	64	V
V_{Ion}	Turn-on input voltage	Increasing input voltage	62	64	66	V
C_I	Internal input capacitance			47		µF
P_O	Output power		0		100	W
η	Efficiency	50% of max I_O		85		%
		max I_O		87		
		50% of max I_O, V_I = 110 V		85		
		max I_O, V_I = 110 V		87		
P_d	Power Dissipation	max I_O		18	25	W
P_{li}	Input idling power	I_O = 0 A, V_I = 110 V		1.0		W
P_{RC}	Input standby power	V_I = 110 V (turned off with RC)		0.7		W
f_s	Switching frequency	0-100 % of max I_O	238	280	322	kHz

V_{Oi}	Output voltage initial setting and accuracy	T_{P1} = +25°C, V_I = 110 V, I_O = 20 A	4.94	5	5.06	V
V_O	Output adjust range	See operating information	4.5	5	5.5	V
	Output voltage tolerance band	0-100% of max I_O	4.75		5.25	V
	Idling voltage	I_O = 0 A	4.75		5.25	V
	Line regulation	max I_O		10	25	mV
	Load regulation	V_I = 110 V, 25-100% of max I_O		20	50	mV
V_{tr}	Load transient voltage deviation	V_I = 110 V, Load step 50-75-50% of max I_O, di/dt = 100mA/µs		±170	±500	mV
t_{tr}	Load transient recovery time			50	500	µs
t_r	Ramp-up time (from 10−90% of V_{Oi})	100% of max I_O			15	ms
t_s	Start-up time (from V_I connection to 90% of V_{Oi})				60	ms
t_{RC}	RC start-up time (from V_{RC} connection to 90% of V_{Oi})	max I_O		1.5	10	ms
RC	Sink current	See operating information	10			mA
	Trigger level	Decreasing / Increasing RC-voltage		0.8/2.5		V
I_O	Output current		0		20	A
I_{lim}	Current limit threshold	V_I = 110 V, T_{P1} < max T_{P1}		32	40	A
I_{sc}	Short circuit current	T_{P1} = 25ºC, see Note 1		0.04	0.1	A
C_{out}	Recommended Capacitive Load	T_{P1} = 25ºC, see Note 2	0		2000	µF
V_{Oac}	Output ripple & noise	See ripple & noise section, V_{Oi}		35	100	mVp-p
OVP	Over voltage protection	T_{P1} = +25°C, V_I = 110 V, 0-100% of max I_O		7		V

Note 1: hiccup mode

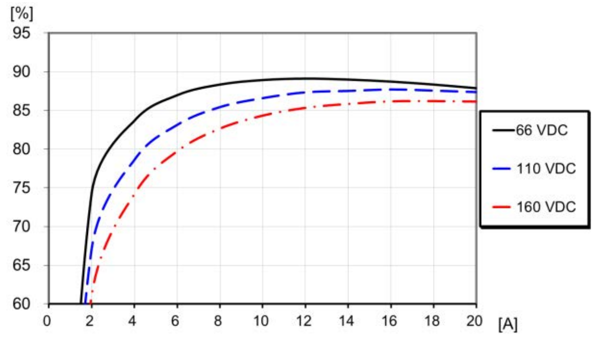
Note 2: Test condition: Electronic Capacitor and full load

PKM 7000A series Direct Converters Input 66-160 V, Output up to 12.5 A / 150 W	28701-BMR 711 Rev. B October 2017 © Flex

Typical Characteristics
5 V, 20 A / 100 W

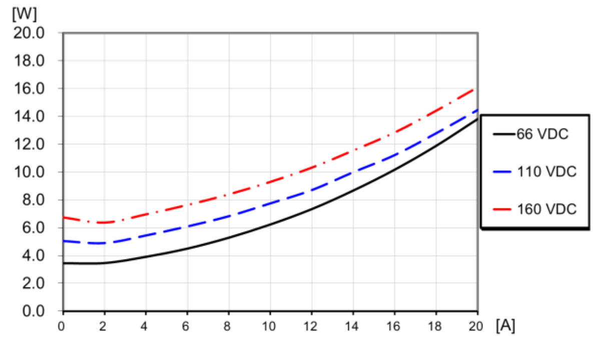
PKM 7111A PIP

Efficiency



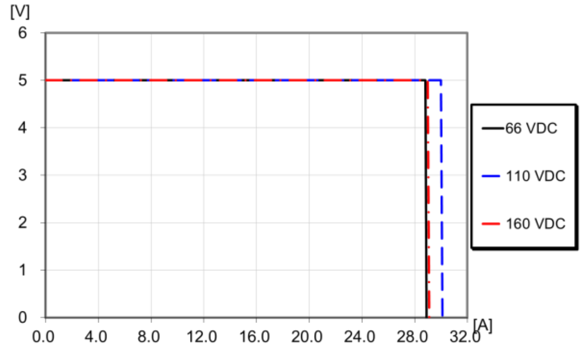
Efficiency vs. load current and input voltage at +25°C.

Power Dissipation



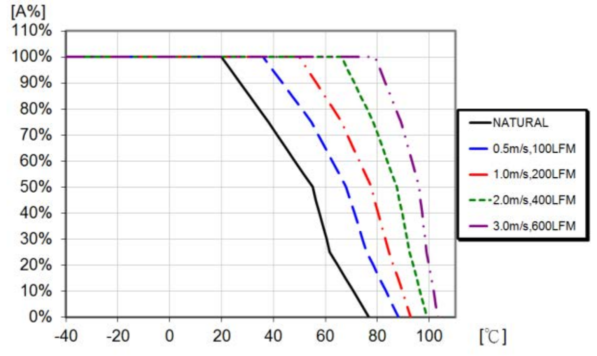
Dissipated power vs. load current and input voltage at +25°C.

Current Limit Characteristics



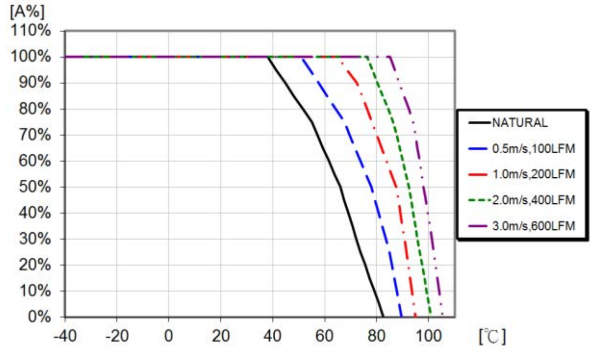
Output voltage vs. load current at I_O > max I_O at +25°C.

Output Current Derating(10mm ¼ brick heat sink)



Available load current vs. ambient air temperature and airflow at V_I=110 V. See Thermal Consideration section.

Output Current Derating(20mm ¼ brick heat sink)

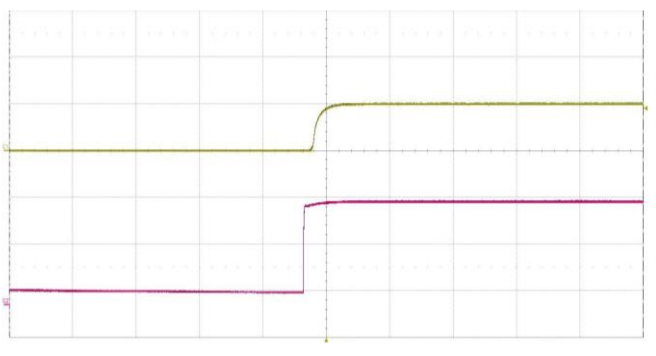


Available load current vs. ambient air temperature and airflow at V_I=110 V. See Thermal Consideration section.

Typical Characteristics
5 V, 20 A / 100 W

PKM 7111A PIP

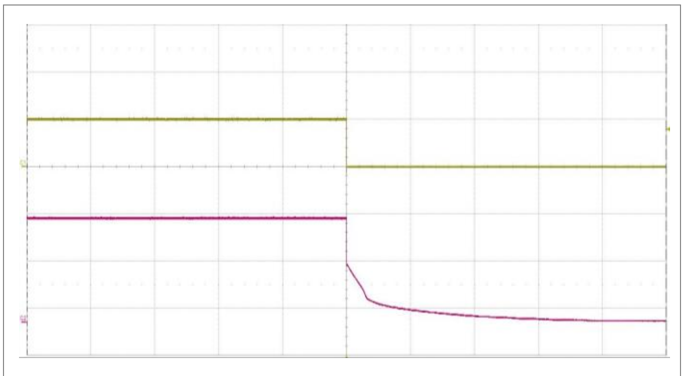
Start-up



Start-up enabled by connecting V_I at:
T_{P1} = +25°C, V_I = 110 V,
I_O = 20 A resistive load.

Top trace: output voltage (5 V/div.).
Bottom trace: input voltage (50 V/div).
Time scale: (50 ms/div.).

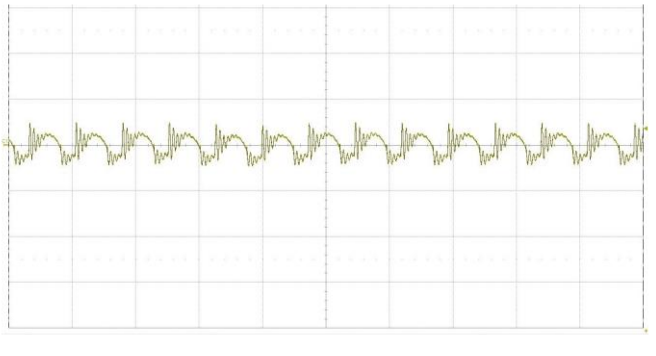
Shut-down



Start-up enabled by connecting V_I at:
T_{P1} = +25°C, V_I = 110 V,
I_O = 20 A resistive load.

Top trace: output voltage (5 V/div.).
Bottom trace: input voltage (50 V/div.).
Time scale: (1 s/div.).

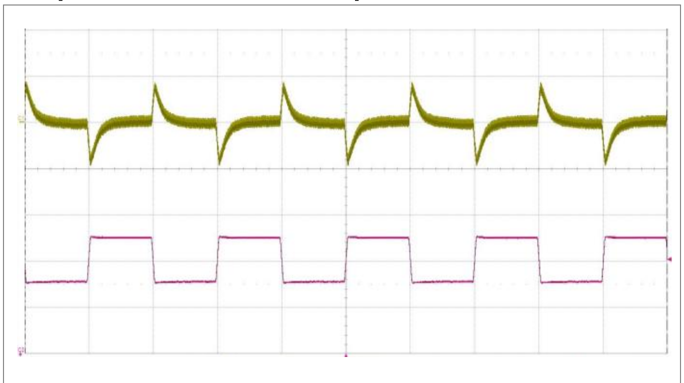
Output Ripple & Noise



Output voltage ripple at:
T_{P1} = +25°C, VI =110 V,
I_O = 20 A resistive load.

Trace: output voltage (100 mV/div.).
Time scale: (5 µs/div.).

Output Load Transient Response



Output voltage response to load current step-change (10-15-10 A) at:
T_{P1} = +25°C, V_I = 110 V.

Trace: output voltage (200 mV/div.).
Bottom trace: load current (5 A/div.).
Time scale: (1ms/div.).

Output Voltage Adjust (TRIM UP/TRIM DOWN)

The resistor value for an adjusted output voltage is calculated by using

the following equations:

Output Voltage Adjust, Increase:

$$\mathrm{R}_{\mathrm{TRIM\_UP}} = \left(\frac{5.11 \times V_{o,set} \times (100 + \Delta\%)}{1.225 \times \Delta\%} - \frac{511}{\Delta\%} - 10.22 \right) \mathrm{k\Omega}$$

$$\Delta\% = \left(\frac{V_{desired} - V_{o,set}}{V_{o,set}} \right) \times 100$$

Output Voltage Adjust, Decrease:

$$R_{\mathrm{TRIM\_DOWN}} = \left(\frac{511}{\Delta\%} - 10.22 \right) \mathrm{k\Omega}$$

$$\Delta\% = \left(\frac{V_{o,set} - V_{desired}}{V_{o,set}} \right) \times 100$$

Output Voltage=5.0V

Example:

To trim up the 5.0V model by 8% to 5.4V the required external resistor is:

$$\mathrm{R}_{\mathrm{TRIM\_UP}} = \left(\frac{5.11 \times 5.0 \times (100 + 8)}{1.225 \times 8} - \frac{511}{8} - 10.22 \right) = 207.48\,\mathrm{k\Omega}$$

$$\Delta\% = \left(\frac{5.4 - 5.0}{5.0} \right) \times 100 = 8$$

Example:

To trim down the 5.0V model by 7% to 4.65V the required external resistor is:

$$R_{\mathrm{TRIM\_DOWN}} = \left(\frac{511}{7} - 10.22 \right) = 62.78\ \mathrm{k\Omega}$$

Electrical Specification
12 V, 8.3 A / 100 W

PKM 7113A PIP

TP1 = -40 to 105ºC, VI = 66 to 160 V, {sense pins connected to output pins} unless otherwise specified under Conditions.
Typical values given at: TP1 = +25°C, VI= 110 VI max IO, unless otherwise specified under Conditions.
Additional Cin = 47 µF, Cout = 10uF ceramic Cap. + 22uF E-Cap. See Operating Information section for selection of capacitor types.

Characteristics		Conditions	min	typ	max	Unit
V_I	Input voltage range		66		160	V
V_{Ioff}	Turn-off input voltage	Decreasing input voltage	60	62	64	V
V_{Ion}	Turn-on input voltage	Increasing input voltage	62	64	66	V
C_I	Internal input capacitance			47		µF
P_O	Output power		0		100	W
η	Efficiency	50% of max I_O		87		%
		max I_O		89		
		50% of max I_O, V_I = 110 V		87		
		max I_O, V_I = 110 V		89		
P_d	Power Dissipation	max I_O		11	15	W
P_{li}	Input idling power	I_O = 0 A, V_I = 110 V		1.0		W
P_{RC}	Input standby power	V_I = 110 V (turned off with RC)		0.7		W
f_s	Switching frequency	0-100 % of max I_O	238	280	322	kHz

V_{Oi}	Output voltage initial setting and accuracy	T_{P1} = +25°C, V_I = 110 V, I_O = 8.3 A	11.856	12	12.144	V
V_O	Output adjust range	See operating information	10.8	12	13.2	V
	Output voltage tolerance band	0-100% of max I_O	11.5		12.5	V
	Idling voltage	I_O = 0 A	11.5		12.5	V
	Line regulation	max I_O		10	60	mV
	Load regulation	V_I = 110 V, 25-100% of max I_O		20	120	mV
V_{tr}	Load transient voltage deviation	V_I = 110 V, Load step 50-75-50% of max I_O, di/dt = 100mA/µs		±377	±1000	mV
t_{tr}	Load transient recovery time			70	500	µs
t_r	Ramp-up time (from 10−90% of V_{Oi})	100% of max I_O			15	ms
t_s	Start-up time (from V_I connection to 90% of V_{Oi})				60	ms
t_{RC}	RC start-up time (from V_{RC} connection to 90% of V_{Oi})	max I_O		1.5	10	ms
RC	Sink current	See operating information	10			mA
	Trigger level	Decreasing / Increasing RC-voltage		0.8/2.5		V
I_O	Output current		0		8.3	A
I_{lim}	Current limit threshold	V_I = 110 V, T_{P1} < max T_{P1}		13	16	A
I_{sc}	Short circuit current	T_{P1} = 25ºC, see Note 1		0.04	0.1	A
C_{out}	Recommended Capacitive Load	T_{P1} = 25ºC, see Note 2	0		2000	µF
V_{Oac}	Output ripple & noise	See ripple & noise section, V_{Oi}		16	150	mVp-p
OVP	Over voltage protection	T_{P1} = +25°C, V_I = 110 V, 0-100% of max I_O		15		V

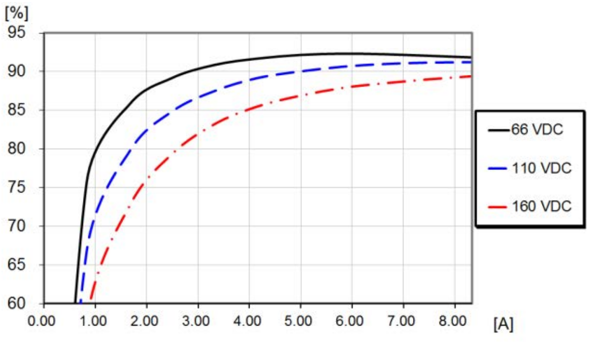
Note 1: hiccup mode

Note 2: Test condition: Electronic Capacitor and full load

Typical Characteristics
12 V, 8.3 A / 100 W

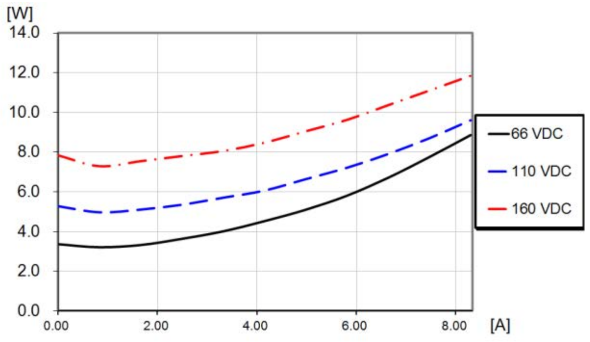
PKM 7113A PIP

Efficiency



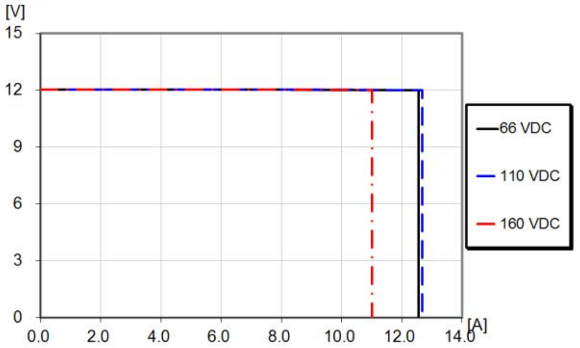
Efficiency vs. load current and input voltage at +25°C.

Power Dissipation



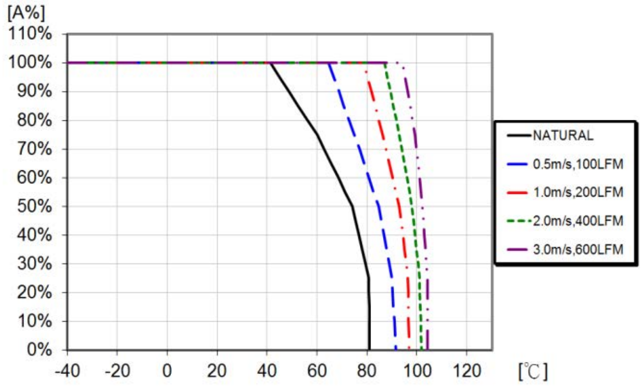
Dissipated power vs. load current and input voltage at +25°C.

Current Limit Characteristics



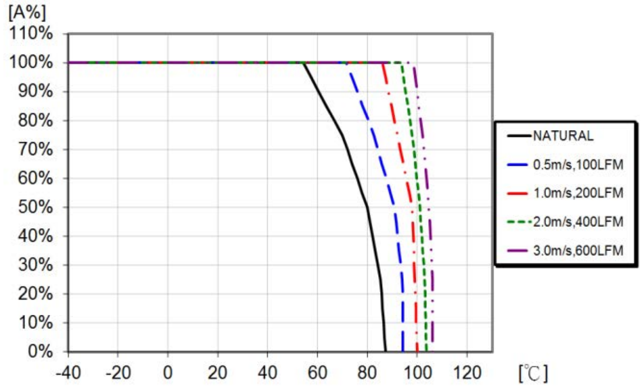
Output voltage vs. load current at I_O > max I_O at +25°C.

Output Current Derating(10mm ¼ brick heat sink)



Available load current vs. ambient air temperature and airflow at V_I=110 V. See Thermal Consideration section.

Output Current Derating(20mm ¼ brick heat sink)

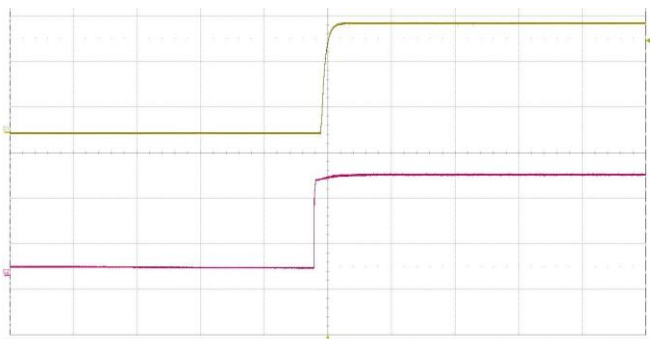


Available load current vs. ambient air temperature and airflow at V_I=110 V. See Thermal Consideration section.

Typical Characteristics
12 V, 8.3 A / 100 W

PKM 7113A PIP

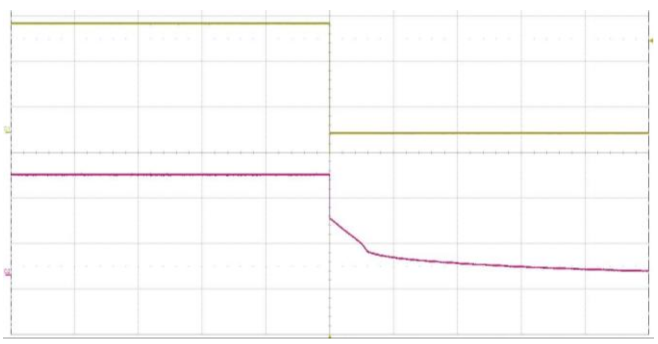
Start-up



Start-up enabled by connecting V_I at:
T_{P1} = +25°C, V_I = 110 V,
I_O = 8.3 A resistive load.

Top trace: output voltage (5 V/div.).
Bottom trace: input voltage (50 V/div).
Time scale: (50 ms/div.).

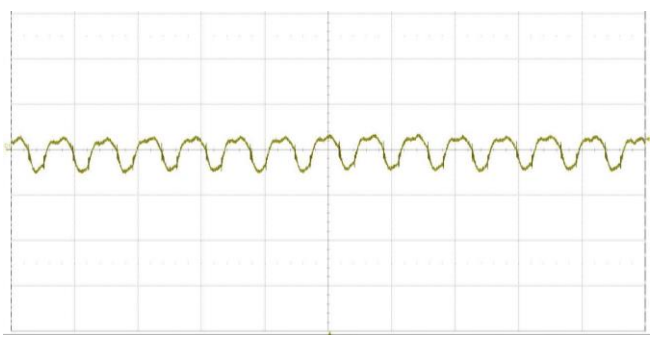
Shut-down



Start-up enabled by connecting V_I at:
T_{P1} = +25°C, V_I = 110 V,
I_O = 8.3 A resistive load.

Top trace: output voltage (5 V/div.).
Bottom trace: input voltage (50 V/div.).
Time scale: (500 ms/div.).

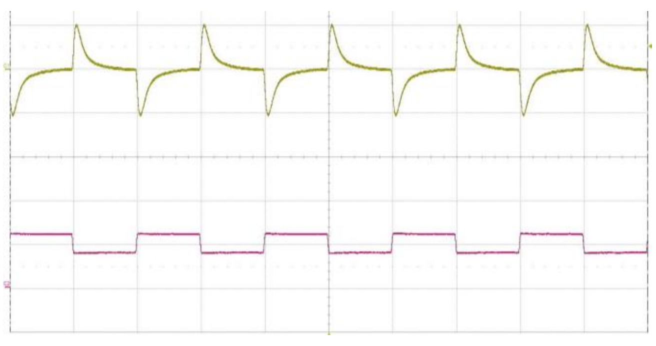
Output Ripple & Noise



Output voltage ripple at:
T_{P1} = +25°C, VI =110 V,
I_O = 8.3 A resistive load.

Trace: output voltage (50 mV/div.).
Time scale: (5 µs/div.).

Output Load Transient Response



Output voltage response to load current step-change (4.15-6.225-4.15 A) at:
T_{P1} = +25°C, V_I = 110 V.

Trace: output voltage (500 mV/div.).
Bottom trace: load current (5 A/div.).
Time scale: (1ms/div.).

Output Voltage Adjust (TRIM UP/TRIM DOWN)

The resistor value for an adjusted output voltage is calculated by using the following equations:

Output Voltage Adjust, Increase:

$$R_{TRIM\_UP} = \left(\frac{5.11 \times V_{o,set} \times (100 + \Delta\%)}{1.225 \times \Delta\%} - \frac{511}{\Delta\%} - 10.22 \right) \text{k}\Omega$$

$$\Delta\% = \left(\frac{V_{desired} - V_{o,set}}{V_{o,set}} \right) \times 100$$

Output Voltage Adjust, Decrease:

$$R_{TRIM\_DOWN} = \left(\frac{5.11}{\Delta\%} - 10.22 \right) \text{k}\Omega$$

$$\Delta\% = \left(\frac{V_{o,set} - V_{desired}}{V_{o,set}} \right) \times 100$$

Output Voltage=12V

Example:

To trim up the 12V model by 8% to 12.96V the required external resistor is:

$$R_{TRIM\_UP} = \left(\frac{5.11 \times 12 \times (100 + 8)}{1.225 \times 8} - \frac{511}{8} - 10.22 \right) = 601.68\,\text{k}\Omega$$

$$\Delta\% = \left(\frac{12.96 - 12}{12} \right) \times 100 = 8$$

Example:

To trim down the 12V model by 7% to 11.16V the required external resistor is:

$$R_{TRIM\_DOWN} = \left(\frac{5.11}{7} - 10.22 \right) = 62.78\ \text{k}\Omega$$

Electrical Specification
15 V, 6.67 A / 100 W

PKM 7115A PIP

TP1 = -40 to 105ºC, VI = 66 to 160 V, {sense pins connected to output pins} unless otherwise specified under Conditions.
Typical values given at: TP1 = +25°C, VI= 110 VI max IO, unless otherwise specified under Conditions.
Additional Cin = 47 µF, Cout = 10uF ceramic Cap. + 22uF E-Cap. See Operating Information section for selection of capacitor types.

Characteristics		Conditions	min	typ	max	Unit
V_I	Input voltage range		66		160	V
V_{Ioff}	Turn-off input voltage	Decreasing input voltage	60	62	64	V
V_{Ion}	Turn-on input voltage	Increasing input voltage	62	64	66	V
C_I	Internal input capacitance			47		µF
P_O	Output power		0		100	W
η	Efficiency	50% of max I_O		87		%
		max I_O		89		
		50% of max I_O, V_I = 110 V		87		
		max I_O, V_I = 110 V		89		
P_d	Power Dissipation	max I_O		15	25	W
P_{li}	Input idling power	I_O = 0 A, V_I = 110 V		1.0		W
P_{RC}	Input standby power	V_I = 110 V (turned off with RC)		0.7		W
f_s	Switching frequency	0-100 % of max I_O	238	280	322	kHz

V_{Oi}	Output voltage initial setting and accuracy	T_{P1} = +25°C, V_I = 110 V, I_O = 6.67 A	14.82	15	15.18	V
V_O	Output adjust range	See operating information	13.5	15	16.5	V
	Output voltage tolerance band	0-100% of max I_O	14.5		15.5	V
	Idling voltage	I_O = 0 A	14.5		15.5	V
	Line regulation	max I_O		10	75	mV
	Load regulation	V_I = 110 V, 25-100% of max I_O		20	150	mV
V_{tr}	Load transient voltage deviation	V_I = 110 V, Load step 50-75-50% of max I_O, di/dt = 100mA/µs		±377	±1000	mV
t_{tr}	Load transient recovery time			70	500	µs
t_r	Ramp-up time (from 10−90% of V_{Oi})	100% of max I_O			15	ms
t_s	Start-up time (from V_I connection to 90% of V_{Oi})				60	ms
t_{RC}	RC start-up time (from V_{RC} connection to 90% of V_{Oi})	max I_O		1.5	10	ms
RC	Sink current	See operating information	10			mA
	Trigger level	Decreasing / Increasing RC-voltage		0.8/2.5		V
I_O	Output current		0		6.67	A
I_{lim}	Current limit threshold	V_I = 110 V, T_{P1} < max T_{P1}		10	12	A
I_{sc}	Short circuit current	T_{P1} = 25ºC, see Note 1		0.04	0.1	A
C_{out}	Recommended Capacitive Load	T_{P1} = 25ºC, see Note 2	0		2000	µF
V_{Oac}	Output ripple & noise	See ripple & noise section, V_{Oi}		44	150	mVp-p
OVP	Over voltage protection	T_{P1} = +25°C, V_I = 110 V, 0-100% of max I_O		18		V

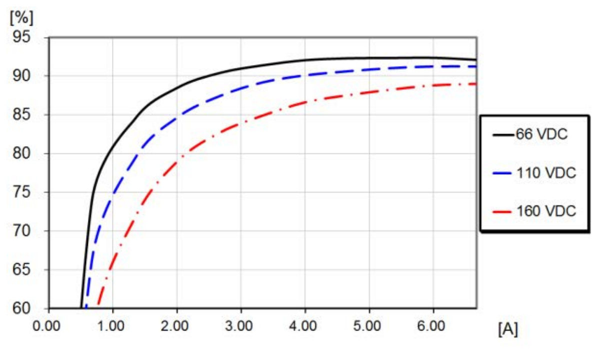
Note 1: hiccup mode

Note 2: Test condition: Electronic Capacitor and full load

Typical Characteristics
15 V, 6.67 A / 100 W

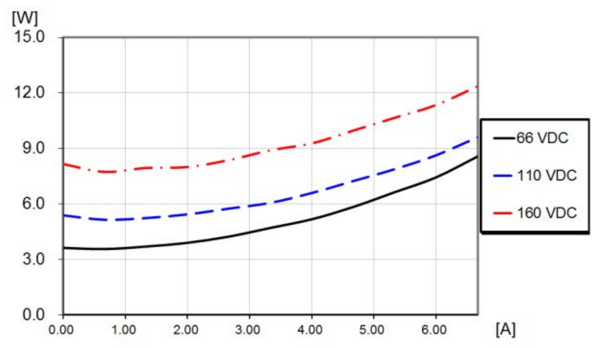
PKM 7115A PIP

Efficiency



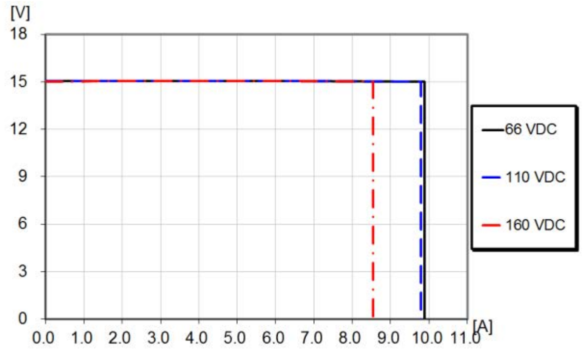
Efficiency vs. load current and input voltage at +25°C.

Power Dissipation



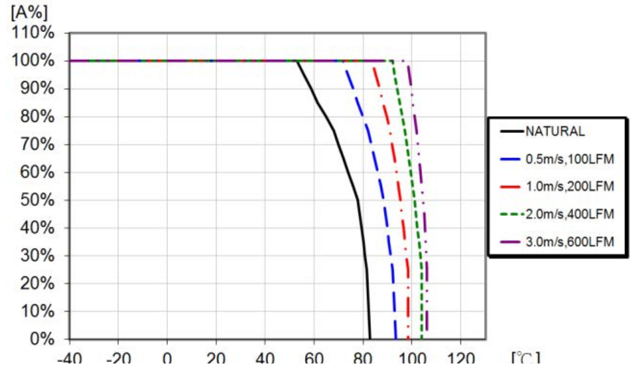
Dissipated power vs. load current and input voltage at +25°C.

Current Limit Characteristics



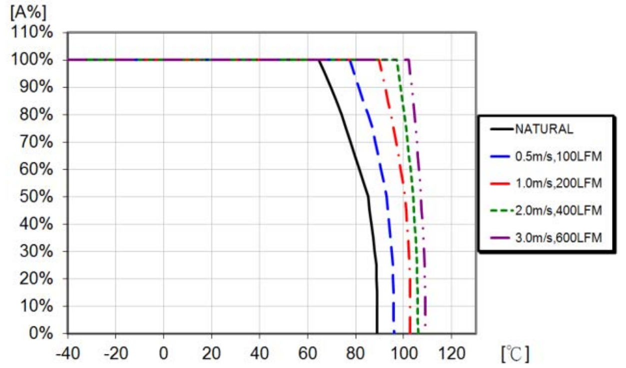
Output voltage vs. load current at I_O > max I_O at +25°C.

Output Current Derating(10mm ¼ brick heat sink)



Available load current vs. ambient air temperature and airflow at V_I=110 V. See Thermal Consideration section.

Output Current Derating(20mm ¼ brick heat sink)

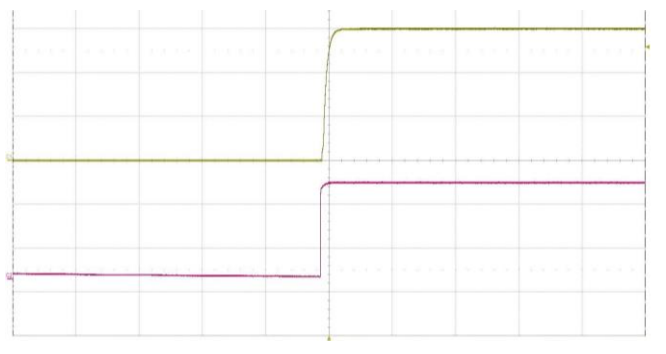


Available load current vs. ambient air temperature and airflow at V_I=110 V. See Thermal Consideration section.

Typical Characteristics
15 V, 6.67 A / 100 W

PKM 7115A PIP

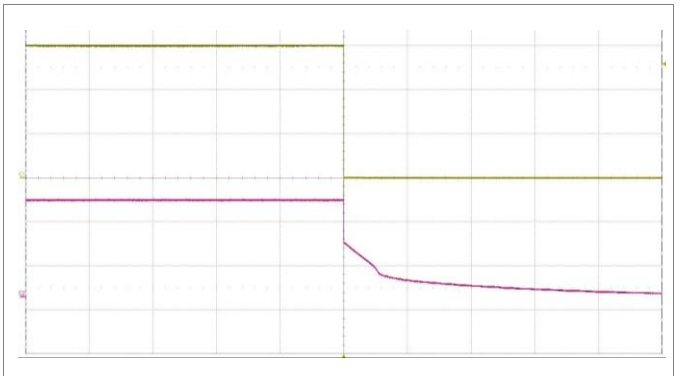
Start-up



Start-up enabled by connecting V_I at:
T_{P1} = +25°C, V_I = 110 V,
I_O = 6.67 A resistive load.

Top trace: output voltage (5 V/div.).
Bottom trace: input voltage (50 V/div).
Time scale: (200 ms/div.).

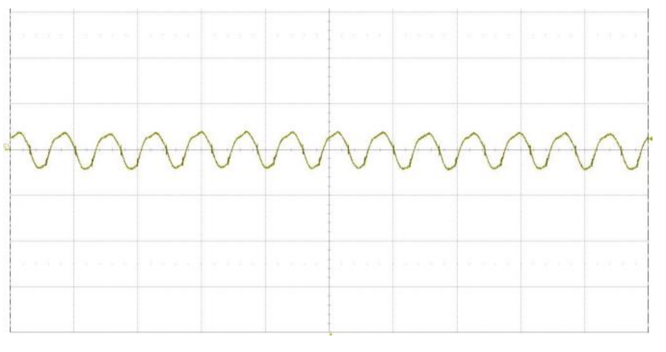
Shut-down



Start-up enabled by connecting V_I at:
T_{P1} = +25°C, V_I = 110 V,
I_O = 6.67 A resistive load.

Top trace: output voltage (5 V/div.).
Bottom trace: input voltage (50 V/div.).
Time scale: (500 ms/div.).

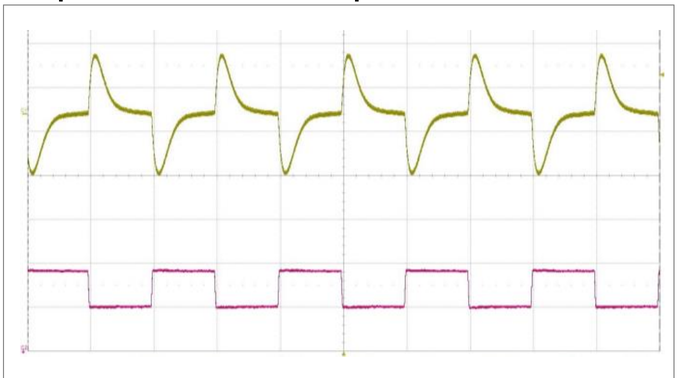
Output Ripple & Noise



Output voltage ripple at:
T_{P1} = +25°C, VI =110 V,
I_O = 6.67 A resistive load.

Trace: output voltage (50 mV/div.).
Time scale: (5 µs/div.).

Output Load Transient Response



Output voltage response to load current step-change (3.335-5.003-3.335 A) at:
T_{P1} = +25°C, V_I = 110 V.

Trace: output voltage (500 mV/div.).
Bottom trace: load current (2 A/div.).
Time scale: (1ms/div.).

Output Voltage Adjust (TRIM UP/TRIM DOWN)

The resistor value for an adjusted output voltage is calculated by using

the following equations:

Output Voltage Adjust, Increase:

$$R_{TRIM\_UP} = \left(\frac{5.11 \times V_{o,set} \times (100 + \Delta\%)}{1.225 \times \Delta\%} - \frac{511}{\Delta\%} - 10.22 \right) \text{k}\Omega$$

$$\Delta\% = \left(\frac{V_{desired} - V_{o,set}}{V_{o,set}} \right) \times 100$$

Output Voltage Adjust, Decrease:

$$R_{TRIM\_DOWN} = \left(\frac{511}{\Delta\%} - 10.22 \right) \text{k}\Omega$$

$$\Delta\% = \left(\frac{V_{o,set} - V_{desired}}{V_{o,set}} \right) \times 100$$

Output Voltage=15V

Example:

To trim up the 15V model by 8% to 16.2V the required external resistor is:

$$R_{TRIM\_UP} = \left(\frac{5.11 \times 15 \times (100 + 8)}{1.225 \times 8} - \frac{511}{8} - 10.22 \right) = 770.62 \text{ k}\Omega$$

$$\Delta\% = \left(\frac{16.2 - 15}{15} \right) \times 100 = 8$$

Example:

To trim down the 15V model by 7% to 13.95V the required external resistor is:

$$R_{TRIM\_DOWN} = \left(\frac{511}{7} - 10.22 \right) = 62.78 \text{ k}\Omega$$

Electrical Specification
24 V, 4.16 A / 100 W

PKM 7116ZA PIP

TP1 = -40 to 105ºC, VI = 66 to 160 V, {sense pins connected to output pins} unless otherwise specified under Conditions.
Typical values given at: TP1 = +25°C, VI= 110 VI max IO, unless otherwise specified under Conditions.
Additional Cin = 47 µF, Cout = 10uF ceramic Cap. + 22uF E-Cap. See Operating Information section for selection of capacitor types.

Characteristics		Conditions	min	typ	max	Unit
V_I	Input voltage range		66		160	V
V_{Ioff}	Turn-off input voltage	Decreasing input voltage	60	62	64	V
V_{Ion}	Turn-on input voltage	Increasing input voltage	62	64	66	V
C_I	Internal input capacitance			47		µF
P_O	Output power		0		100	W
η	Efficiency	50% of max I_O		87		%
		max I_O		89		
		50% of max I_O, V_I = 110 V		87		
		max I_O, V_I = 110 V		89		
P_d	Power Dissipation	max I_O		13	17	W
P_{li}	Input idling power	I_O = 0 A, V_I = 110 V		1.0		W
P_{RC}	Input standby power	V_I = 110 V (turned off with RC)		0.7		W
f_s	Switching frequency	0-100 % of max I_O	297.5	350	402.5	kHz

V_{Oi}	Output voltage initial setting and accuracy	T_{P1} = +25°C, V_I = 110 V, I_O = 4.16 A	23.70	24	24.30	V
V_O	Output adjust range	See operating information	21.6	24	26.4	V
	Output voltage tolerance band	0-100% of max I_O	23.4		24.6	V
	Idling voltage	I_O = 0 A	23.4		24.6	V
	Line regulation	max I_O		10	120	mV
	Load regulation	V_I = 110 V, 25-100% of max I_O		20	240	mV
V_{tr}	Load transient voltage deviation	V_I = 110 V, Load step 50-75-50% of max I_O, di/dt = 100mA/µs		±377	±1000	mV
t_{tr}	Load transient recovery time			70	500	µs
t_r	Ramp-up time (from 10−90% of V_{Oi})	100% of max I_O			15	ms
t_s	Start-up time (from V_I connection to 90% of V_{Oi})				60	ms
t_{RC}	RC start-up time (from V_{RC} connection to 90% of V_{Oi})	max I_O		1.5	10	ms
RC	Sink current	See operating information	10			mA
	Trigger level	Decreasing / Increasing RC-voltage		0.8/2.5		V
I_O	Output current		0		4.16	A
I_{lim}	Current limit threshold	V_I = 110 V, T_{P1} < max T_{P1}		9	11	A
I_{sc}	Short circuit current	T_{P1} = 25ºC, see Note 1		0.04	0.1	A
C_{out}	Recommended Capacitive Load	T_{P1} = 25ºC, see Note 2	0		2000	µF
V_{Oac}	Output ripple & noise	See ripple & noise section, V_{Oi}		30.7	500	mVp-p
OVP	Over voltage protection	T_{P1} = +25°C, V_I = 110 V, 0-100% of max I_O		28		V

Note 1: hiccup mode

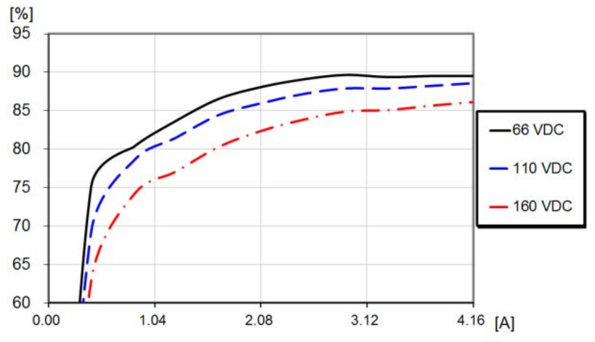
Note 2: Test condition: Electronic Capacitor and full load

PKM 7000A series Direct Converters Input 66-160 V, Output up to 12.5 A / 150 W	28701-BMR 711 Rev. B	October 2017
	© Flex	

Typical Characteristics
24 V, 4.16 A / 100 W

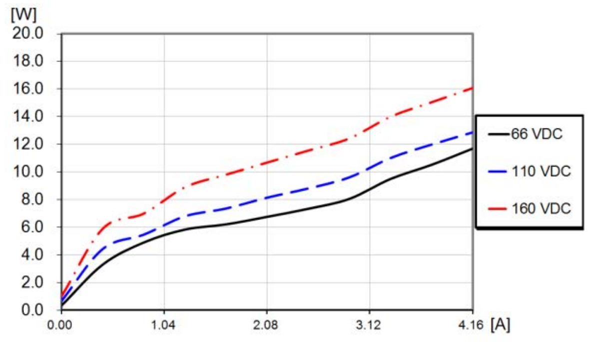
PKM 7116ZA PIP

Efficiency



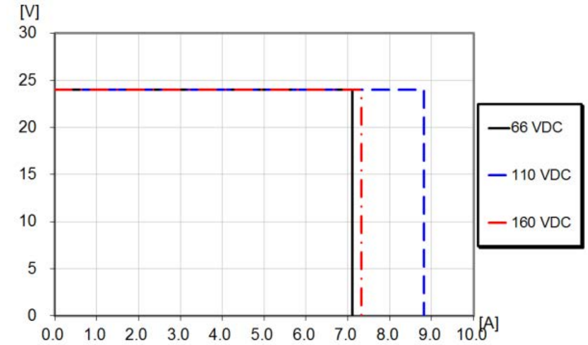
Efficiency vs. load current and input voltage at +25°C.

Power Dissipation



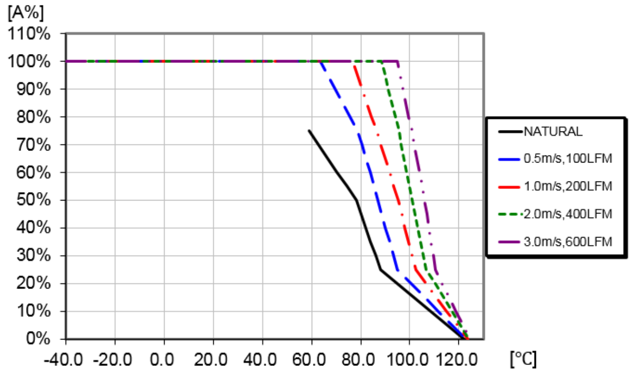
Dissipated power vs. load current and input voltage at +25°C.

Current Limit Characteristics



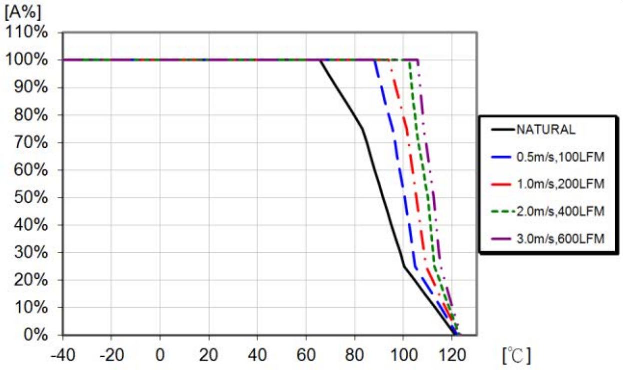
Output voltage vs. load current at I_O > max I_O at +25°C.

Output Current Derating(10mm ¼ brick heat sink)



Available load current vs. ambient air temperature and airflow at V_I=110 V. See Thermal Consideration section.

Output Current Derating(20mm ¼ brick heat sink)



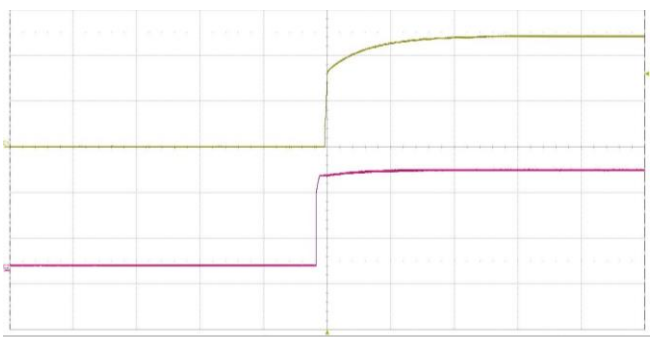
Available load current vs. ambient air temperature and airflow at V_I=110 V. See Thermal Consideration section.

PKM 7000A series Direct Converters Input 66-160 V, Output up to 12.5 A / 150 W	28701-BMR 711 Rev. B October 2017 © Flex

Typical Characteristics
24 V, 4.16 A / 100 W

PKM 7116ZA PIP

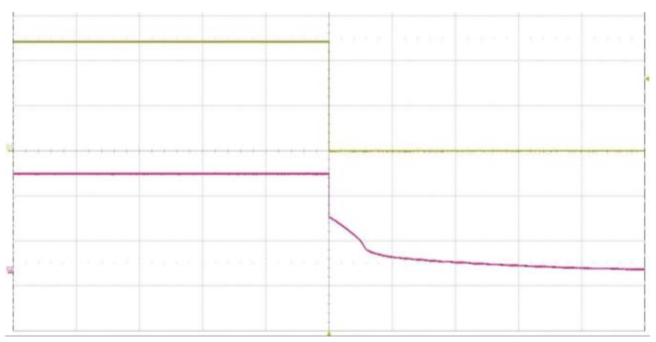
Start-up



Start-up enabled by connecting V_I at:
T_{P1} = +25°C, V_I = 110 V,
I_O = 4.16 A resistive load.

Top trace: output voltage (10 V/div.).
Bottom trace: input voltage (50 V/div).
Time scale: (20 ms/div.).

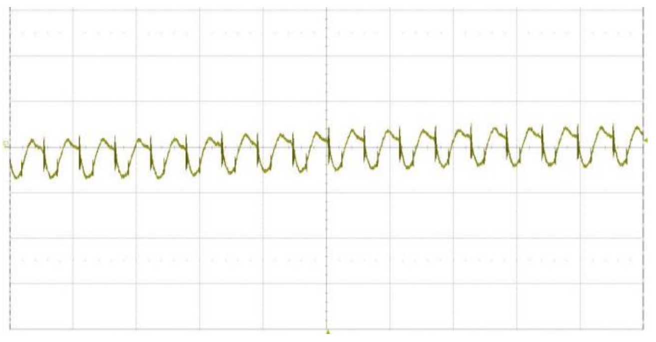
Shut-down



Start-up enabled by connecting V_I at:
T_{P1} = +25°C, V_I = 110 V,
I_O = 4.16 A resistive load.

Top trace: output voltage (10 V/div.).
Bottom trace: input voltage (50 V/div.).
Time scale: (500 ms/div.).

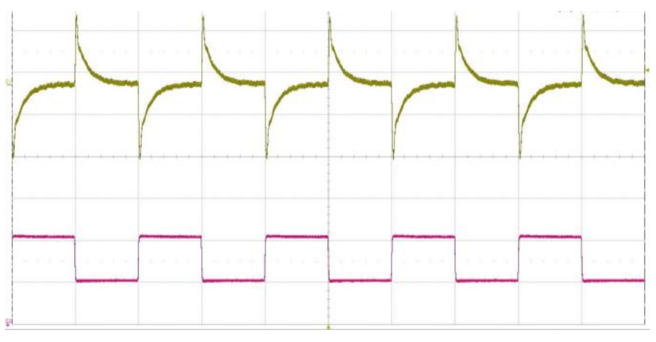
Output Ripple & Noise



Output voltage ripple at:
T_{P1} = +25°C, VI =110 V,
I_O = 4.16 A resistive load.

Trace: output voltage (20 mV/div.).
Time scale: (5 µs/div.).

Output Load Transient Response



Output voltage response to load current step-change (2.08-3.12-2.08 A) at:
T_{P1} = +25°C, V_I = 110 V.

Trace: output voltage (200 mV/div.).
Bottom trace: load current (1 A/div.).
Time scale: (1ms/div.).

Output Voltage Adjust (see operating information)

Output Voltage=24V

The resistor value for an adjusted output voltage is calculated by using the following equations:

Output Voltage Adjust, Increase:

$$R_{ADJ\_UP} = \left(\frac{14.6061}{\Delta} - 120 \right) \text{k}\Omega$$

Output Voltage Adjust, Decrease:

$$R_{ADJ\_DOWN} = \left(\frac{17.2133}{\Delta} - 151.819 \right) \text{k}\Omega$$

Example:

To trim up the 24V model by 8% to 25.92V the required external resistor is:

$$R_{ADJ\_UP} = \left(\frac{14.6061}{0.08} - 120 \right) = 62.58 \text{ k}\Omega$$

Example:

To trim down the 24V model by 7% to 22.32V the required external resistor is:

$$R_{ADJ\_DOWN} = \left(\frac{17.2133}{0.07} - 151.819 \right) = 94.08 \text{ k}\Omega$$

Electrical Specification
48 V, 2.08 A / 100 W

PKM 7116JA PIP

T_{P1} = -40 to 105ºC, V_I = 66 to 160 V, {sense pins connected to output pins} unless otherwise specified under Conditions.
Typical values given at: T_{P1} = +25°C, V_I= 110 V_I max I_O, unless otherwise specified under Conditions.
Additional Cin = 47 µF, Cout = 10uF ceramic Cap. + 22uF E-Cap. See Operating Information section for selection of capacitor types.

Characteristics		Conditions	min	typ	max	Unit
V_I	Input voltage range		66		160	V
V_{Ioff}	Turn-off input voltage	Decreasing input voltage	60	62	64	V
V_{Ion}	Turn-on input voltage	Increasing input voltage	62	64	66	V
C_I	Internal input capacitance			47		µF
P_O	Output power		0		100	W
η	Efficiency	50% of max I_O		87		%
		max I_O		89		
		50% of max I_O, V_I = 110 V		87		
		max I_O, V_I = 110 V		89		
P_d	Power Dissipation	max I_O		12	16	W
P_{li}	Input idling power	I_O = 0 A, V_I = 110 V		1.0		W
P_{RC}	Input standby power	V_I = 110 V (turned off with RC)		0.7		W
f_s	Switching frequency	0-100 % of max I_O	297.5	350	402.5	kHz

V_{Oi}	Output voltage initial setting and accuracy	T_{P1} = +25°C, V_I = 110 V, I_O = 6.25 A	47.4	48	48.6	V
V_O	Output adjust range	See operating information	43.2	48	52.8	V
	Output voltage tolerance band	0-100% of max I_O	46.8		49.2	V
	Idling voltage	I_O = 0 A	46.8		49.2	V
	Line regulation	max I_O		24	240	mV
	Load regulation	V_I = 110 V, 25-100% of max I_O		48	480	mV
V_{tr}	Load transient voltage deviation	V_I = 110 V, Load step 50-75-50% of max I_O, di/dt = 100mA/µs		±400	±1000	mV
t_{tr}	Load transient recovery time			90	500	µs
t_r	Ramp-up time (from 10−90% of V_{Oi})	100% of max I_O			15	ms
t_s	Start-up time (from V_I connection to 90% of V_{Oi})				60	ms
t_{RC}	RC start-up time (from V_{RC} connection to 90% of V_{Oi})	max I_O		1.55	10	ms
RC	Sink current	See operating information	10			mA
	Trigger level	Decreasing / Increasing RC-voltage		0.8/2.5		V
I_O	Output current		0		2.08	A
I_{lim}	Current limit threshold	V_I = 110 V,T_{P1} < max T_{P1}		3.2	5.2	A
I_{sc}	Short circuit current	T_{P1} = 25ºC, see Note 1		0.05	0.1	A
C_{out}	Recommended Capacitive Load	T_{P1} = 25ºC, see Note 2	0		500	µF
V_{Oac}	Output ripple & noise	See ripple & noise section, V_{Oi}		272	500	mVp-p
OVP	Over voltage protection	T_{P1} = +25°C, V_I = 110 V, 0-100% of max I_O		55		V

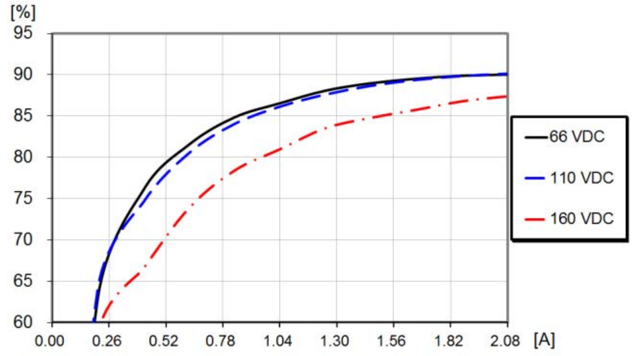
Note 1: hiccup mode

Note 2: Test condition: Electronic Capacitor and full load

Typical Characteristics
48 V, 2.08 A / 100 W

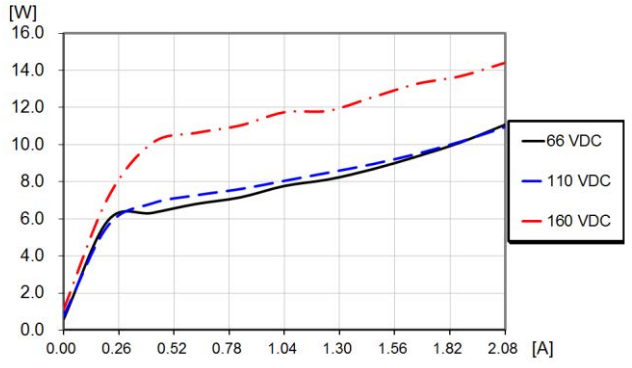
PKM 7116JA PIP

Efficiency



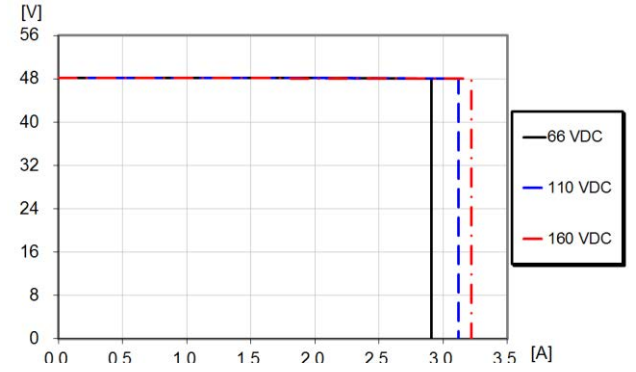
Efficiency vs. load current and input voltage at +25°C.

Power Dissipation



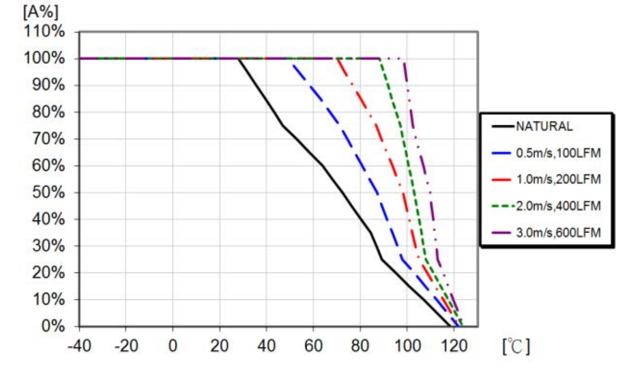
Dissipated power vs. load current and input voltage at +25°C.

Current Limit Characteristics



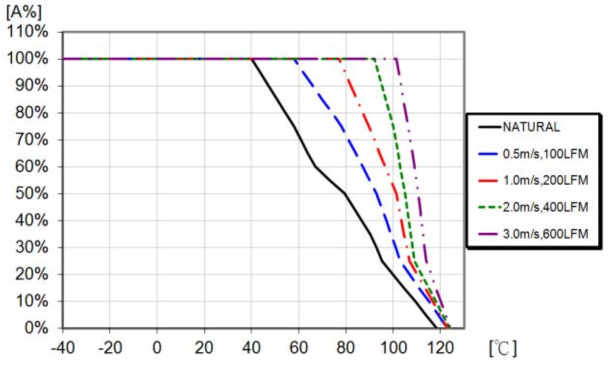
Output voltage vs. load current at I_O > max I_O at +25°C.

Output Current Derating(10mm ¼ brick heat sink)



Available load current vs. ambient air temperature and airflow at V_I=110 V. See Thermal Consideration section.

Output Current Derating(20mm ¼ brick heat sink)

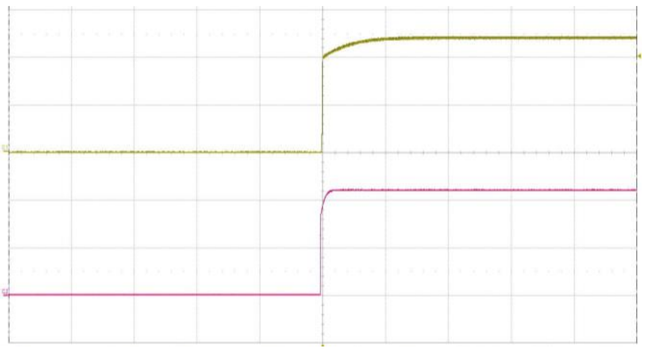


Available load current vs. ambient air temperature and airflow at V_I=110 V. See Thermal Consideration section.

Typical Characteristics
48 V, 2.08 A / 100 W

PKM 7116JA PIP

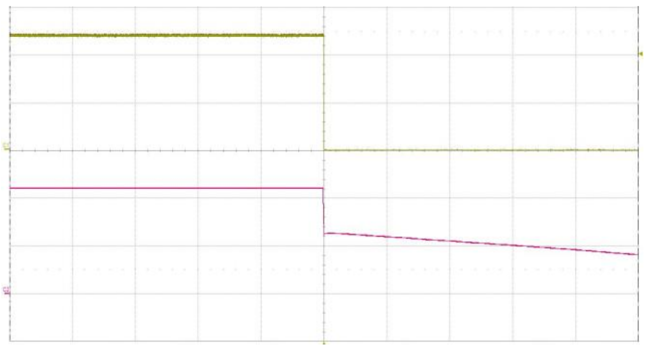
Start-up



Start-up enabled by connecting V_I at:
T_{P1} = +25°C, V_I =110 V,
I_O = 2.08 A resistive load.

Top trace: output voltage (20 V/div.).
Bottom trace: input voltage (50 V/div.).
Time scale: (200 ms/div.).

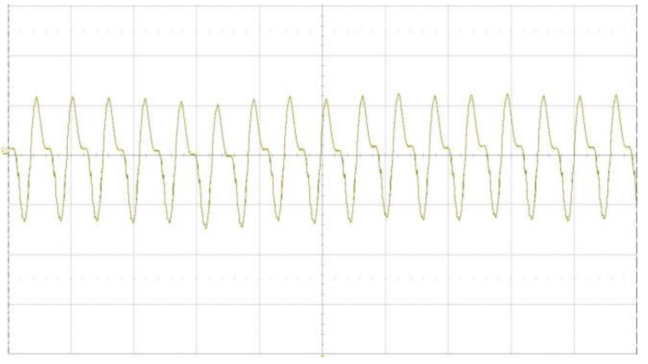
Shut-down



Shut-down enabled by disconnecting V_I at:
T_{P1} = +25°C, V_I = 110 V,
I_O = 2.08A resistive load.

Top trace: output voltage (20 V/div.).
Bottom trace: input voltage (50 V/div.).
Time scale: (200 ms/div.).

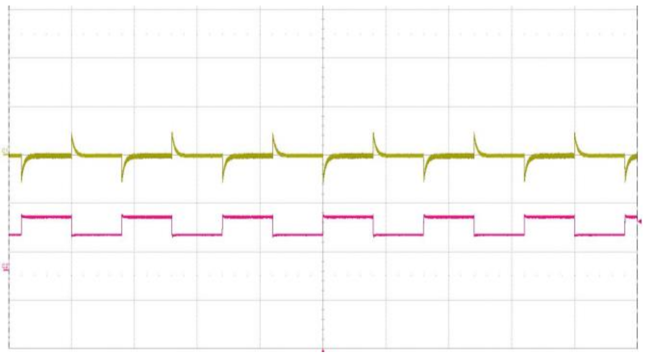
Output Ripple & Noise



Output voltage ripple at:
T_{P1} = +25°C, VI = 110 V,
I_O = 2.08 A resistive load.

Trace: output voltage (100 mV/div.).
Time scale: (5 µs/div.).

Output Load Transient Response



Output voltage response to load current step-change (1.04-1.56-1.04 A) at:
T_{P1} = +25°C, V_I = 110 V.

Top trace: output voltage (2 V/div.).
Bottom trace: load current (1 A/div.).
Time scale: (10ms/div.).

Output Voltage Adjust (TRIM UP/TRIM DOWN)

The resistor value for an adjusted output voltage is calculated by using the following equations:

Output Voltage Adjust, Increase:

$$R_{ADJ\_UP} = \left(\frac{29.2214}{\Delta} - 240 \right) \text{ k}\Omega$$

Output Voltage Adjust, Decrease:

$$R_{ADJ\_DOWN} = \left(\frac{34.4607}{\Delta} - 303.682 \right) \text{ k}\Omega$$

Output Voltage=48V

Example:

To trim up the 48V model by 8% to 51.84V the required external resistor is:

$$R_{ADJ\_UP} = \left(\frac{29.2214}{0.08} - 240 \right) = 125.27 \text{ k}\Omega$$

Example:

To trim down the 48V model by 7% to 44.64V the required external resistor is:

$$R_{ADJ\_DOWN} = \left(\frac{34.4607}{0.07} - 303.682 \right) = 188.61 \text{ k}\Omega$$

Electrical Specification
12 V, 12.5 A / 150 W

PKM 7213A PIP

T_{P1} = -40 to 105ºC, V_I = 66 to 160 V, {sense pins connected to output pins} unless otherwise specified under Conditions.
Typical values given at: T_{P1} = +25°C, V_I= 110 V_I max I_O, unless otherwise specified under Conditions.
Additional Cin = 47 µF, Cout = 10uF ceramic Cap. + 22uF E-Cap. See Operating Information section for selection of capacitor types.

Characteristics		Conditions	min	typ	max	Unit
V_I	Input voltage range		66		160	V
V_{Ioff}	Turn-off input voltage	Decreasing input voltage	60	62	64	V
V_{Ion}	Turn-on input voltage	Increasing input voltage	62	64	66	V
C_I	Internal input capacitance			47		µF
P_O	Output power		0		150	W
η	Efficiency	50% of max I_O		86		%
		max I_O		88		
		50% of max I_O, V_I = 110 V		86		
		max I_O, V_I = 110 V		88		
P_d	Power Dissipation	max I_O		18.5	25	W
P_{li}	Input idling power	I_O = 0 A, V_I = 110 V		1.0		W
P_{RC}	Input standby power	V_I = 110 V (turned off with RC)		0.7		W
f_s	Switching frequency	0-100 % of max I_O	238	280	322	kHz

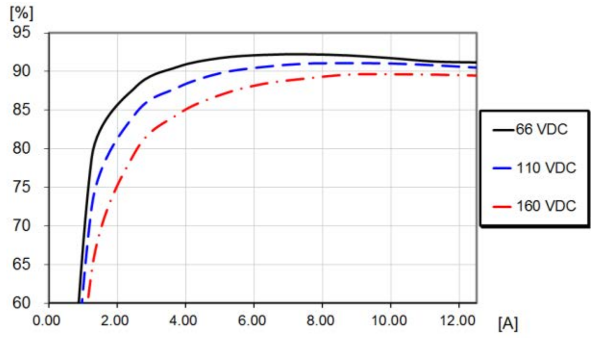
V_{Oi}	Output voltage initial setting and accuracy	T_{P1} = +25°C, V_I = 110 V, I_O = 12.5 A	11.856	12	12.144	V
V_O	Output adjust range	See operating information	10.8	12	13.2	V
	Output voltage tolerance band	0-100% of max I_O	11.5		12.5	V
	Idling voltage	I_O = 0 A	11.5		12.5	V
	Line regulation	max I_O		10	60	mV
	Load regulation	V_I = 110 V, 25-100% of max I_O		20	120	mV
V_{tr}	Load transient voltage deviation	V_I = 110 V, Load step 50-75-50% of max I_O, di/dt = 100mA/µs		±377	±1000	mV
t_{tr}	Load transient recovery time			70	500	µs
t_r	Ramp-up time (from 10−90% of V_{Oi})	100% of max I_O			15	ms
t_s	Start-up time (from V_I connection to 90% of V_{Oi})				60	ms
t_{RC}	RC start-up time (from V_{RC} connection to 90% of V_{Oi})	max I_O		1.55	10	ms
RC	Sink current	See operating information	10			mA
	Trigger level	Decreasing / Increasing RC-voltage		0.8/2.5		V
I_O	Output current		0		12.5	A
I_{lim}	Current limit threshold	V_I = 110 V,T_{P1} < max T_{P1}		16.5	20	A
I_{sc}	Short circuit current	T_{P1} = 25ºC, see Note 1		0.05	0.1	A
C_{out}	Recommended Capacitive Load	T_{P1} = 25ºC, see Note 2	0		2000	µF
V_{Oac}	Output ripple & noise	See ripple & noise section, V_{Oi}		17	150	mVp-p
OVP	Over voltage protection	T_{P1} = +25°C, V_I = 110 V, 0-100% of max I_O		15		V

Note 1: hiccup mode
Note 2: Test condition: Electronic Capacitor and full load

Typical Characteristics 12 V, 12.5 A / 150 W

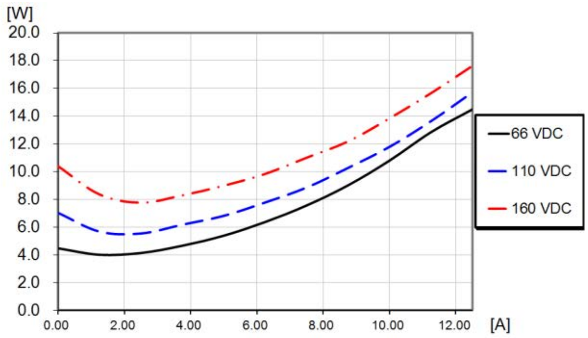
PKM 7213A PIP

Efficiency



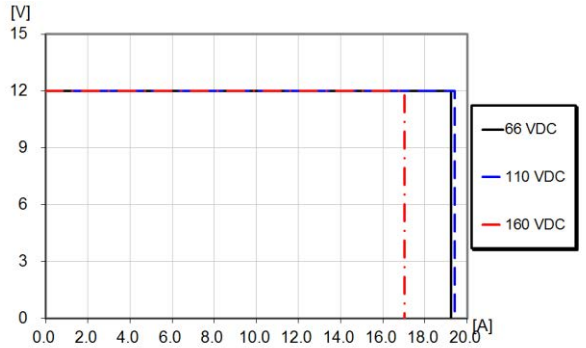
Efficiency vs. load current and input voltage at +25°C.

Power Dissipation



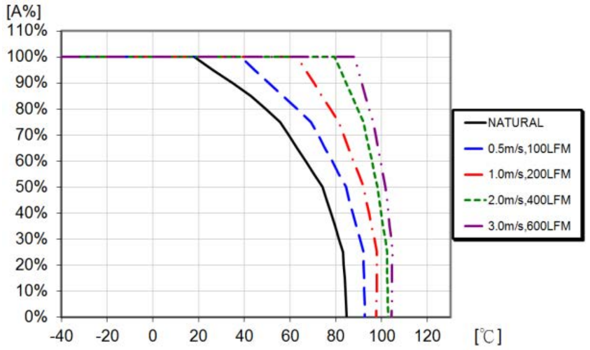
Dissipated power vs. load current and input voltage at +25°C.

Current Limit Characteristics



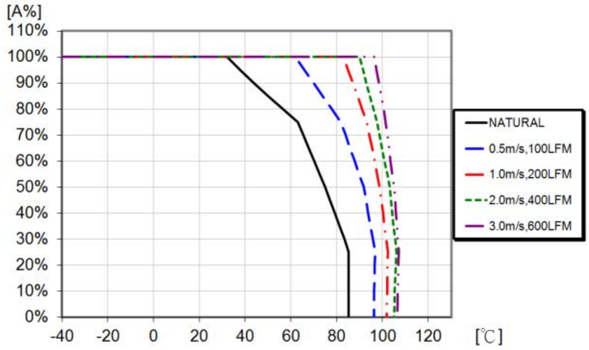
Output voltage vs. load current at I_O > max I_O at +25°C.

Output Current Derating(20mm ¼ brick heat sink)



Available load current vs. ambient air temperature and airflow at V_I=110 V. See Thermal Consideration section.

Output Current Derating(20mm ½ brick heat sink)

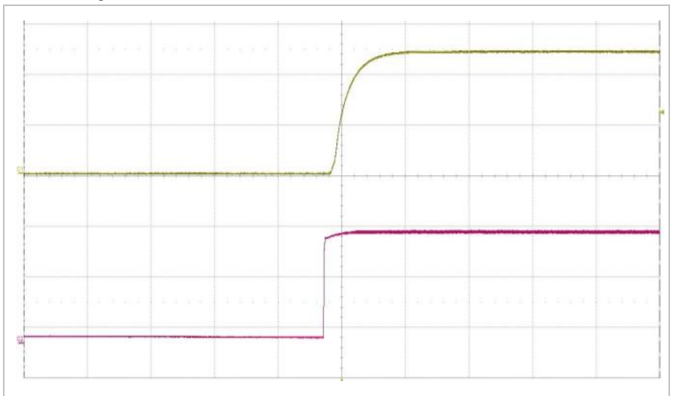


Available load current vs. ambient air temperature and airflow at V_I=110 V. See Thermal Consideration section.

Typical Characteristics
12 V, 12.5 A / 150 W

PKM 7213A PIP

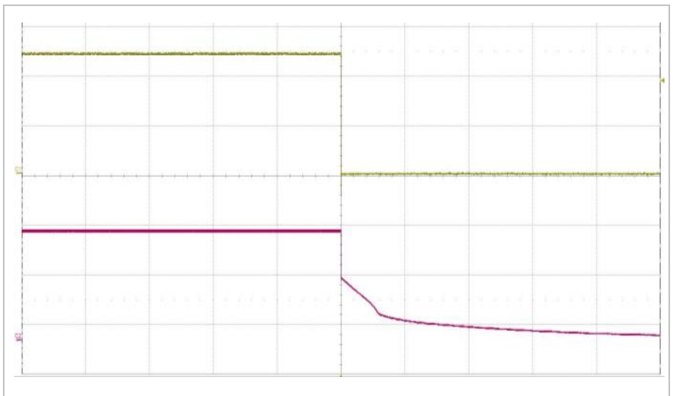
Start-up



Start-up enabled by connecting V_I at:
T_{P1} = +25°C, V_I =110 V,
I_O = 12.5 A resistive load.

Top trace: output voltage (5 V/div.).
Bottom trace: input voltage (50 V/div.).
Time scale: (50 ms/div.).

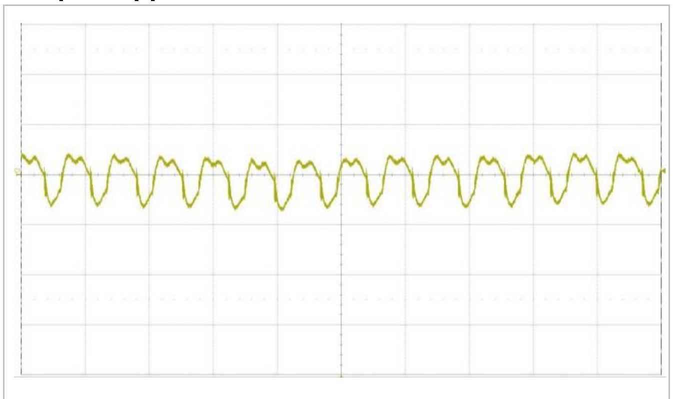
Shut-down



Shut-down enabled by disconnecting V_I at:
T_{P1} = +25°C, V_I = 110 V,
I_O = 12.5 A resistive load.

Top trace: output voltage (5 V/div.).
Bottom trace: input voltage (50 V/div.).
Time scale: (500 ms/div.).

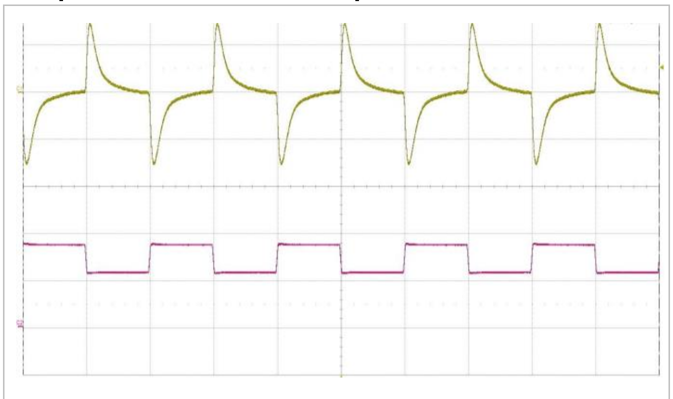
Output Ripple & Noise



Output voltage ripple at:
T_{P1} = +25°C, VI = 110 V,
I_O = 12.5 A resistive load.

Trace: output voltage (20 mV/div.).
Time scale: (5 µs/div.).

Output Load Transient Response



Output voltage response to load current step-change (6.25-9.375-6.25 A) at:
T_{P1} = +25°C, V_I = 110 V.

Top trace: output voltage (500 mV/div.).
Bottom trace: load current (5 A/div.).
Time scale: (1ms/div.).

Output Voltage Adjust (TRIM UP/TRIM DOWN)

The resistor value for an adjusted output voltage is calculated by using the following equations:

Output Voltage Adjust, Increase:

$$R_{TRIM\_UP} = \left(\frac{5.11 \times V_{o,set} \times (100 + \Delta\%)}{1.225 \times \Delta\%} - \frac{511}{\Delta\%} - 10.22 \right) \text{ k}\Omega$$

$$\Delta\% = \left(\frac{V_{desired} - V_{o,set}}{V_{o,set}} \right) \times 100$$

Output Voltage Adjust, Decrease:

$$R_{TRIM\_DOWN} = \left(\frac{5.11}{\Delta\%} - 10.22 \right) \text{ k}\Omega$$

$$\Delta\% = \left(\frac{V_{o,set} - V_{desired}}{V_{o,set}} \right) \times 100$$

Output Voltage=12V

Example:
To trim up the 12V model by 8% to 12.96V the required external resistor is:

$$R_{TRIM\_UP} = \left(\frac{5.11 \times 12 \times (100 + 8)}{1.225 \times 8} - \frac{511}{8} - 10.22 \right) = 601.68 \text{ k}\Omega$$

$$\Delta\% = \left(\frac{12.96 - 12}{12} \right) \times 100 = 8$$

Example:
To trim down the 12V model by 7% to 11.16V the required external resistor is:

$$R_{TRIM\_DOWN} = \left(\frac{5.11}{7} - 10.22 \right) = 62.78 \text{ k}\Omega$$

Electrical Specification
15 V, 10 A / 150 W

PKM 7215A PIP

T_{P1} = -40 to 105ºC, V_I = 66 to 160 V, {sense pins connected to output pins} unless otherwise specified under Conditions.
Typical values given at: T_{P1} = +25°C, V_I= 110 V_I max I_O, unless otherwise specified under Conditions.
Additional Cin = 47 µF, Cout = 10uF ceramic Cap. + 22uF E-Cap. See Operating Information section for selection of capacitor types.

Characteristics		Conditions	min	typ	max	Unit
V_I	Input voltage range		66		160	V
V_{Ioff}	Turn-off input voltage	Decreasing input voltage	60	62	64	V
V_{Ion}	Turn-on input voltage	Increasing input voltage	62	64	66	V
C_I	Internal input capacitance			47		µF
P_O	Output power		0		150	W
η	Efficiency	50% of max I_O		86		%
		max I_O		88		
		50% of max I_O, V_I = 110 V		86		
		max I_O, V_I = 110 V		88		
P_d	Power Dissipation	max I_O		22	30	W
P_{li}	Input idling power	I_O = 0 A, V_I = 110 V		1.0		W
P_{RC}	Input standby power	V_I = 110 V (turned off with RC)		0.7		W
f_s	Switching frequency	0-100 % of max I_O	238	280	322	kHz

V_{Oi}	Output voltage initial setting and accuracy	T_{P1} = +25°C, V_I = 110 V, I_O = 10 A	14.82	15	15.18	V
V_O	Output adjust range	See operating information	13.5	15	16.5	V
	Output voltage tolerance band	0-100% of max I_O	14.5		15.5	V
	Idling voltage	I_O = 0 A	14.5		15.5	V
	Line regulation	max I_O		15	75	mV
	Load regulation	V_I = 110 V, 25-100% of max I_O		25	150	mV
V_{tr}	Load transient voltage deviation	V_I = 110 V, Load step 50-75-50% of max I_O, di/dt = 100mA/µs		±400	±1000	mV
t_{tr}	Load transient recovery time	V_I = 110 V, Load step 50-75-50% of max I_O, di/dt = 100mA/µs		90	500	µs
t_r	Ramp-up time (from 10−90% of V_{Oi})	100% of max I_O			15	ms
t_s	Start-up time (from V_I connection to 90% of V_{Oi})	100% of max I_O			60	ms
t_{RC}	RC start-up time (from V_{RC} connection to 90% of V_{Oi})	max I_O		1.55	10	ms
RC	Sink current	See operating information	10			mA
	Trigger level	Decreasing / Increasing RC-voltage		0.8/2.5		V
I_O	Output current		0		10	A
I_{lim}	Current limit threshold	V_I = 110 V, T_{P1} < max T_{P1}		15	18	A
I_{sc}	Short circuit current	T_{P1} = 25ºC, see Note 1		0.05	0.1	A
C_{out}	Recommended Capacitive Load	T_{P1} = 25ºC, see Note 2	0		2000	µF
V_{Oac}	Output ripple & noise	See ripple & noise section, V_{Oi}		43	150	mVp-p
OVP	Over voltage protection	T_{P1} = +25°C, V_I = 110 V, 0-100% of max I_O		18		V

Note 1: hiccup mode

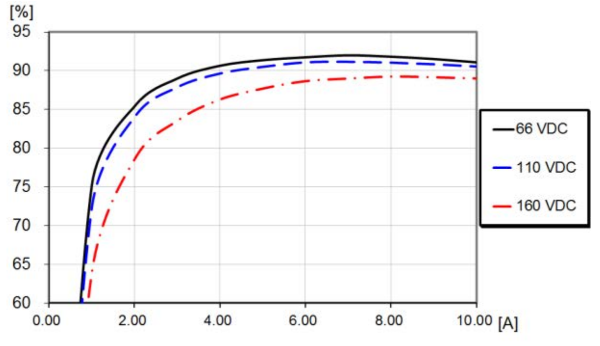
Note 2: Test condition: Electronic Capacitor and full load

PKM 7000A series Direct Converters Input 66-160 V, Output up to 12.5 A / 150 W	28701-BMR 711 Rev. B October 2017 © Flex

Typical Characteristics
15 V, 10 A / 150 W

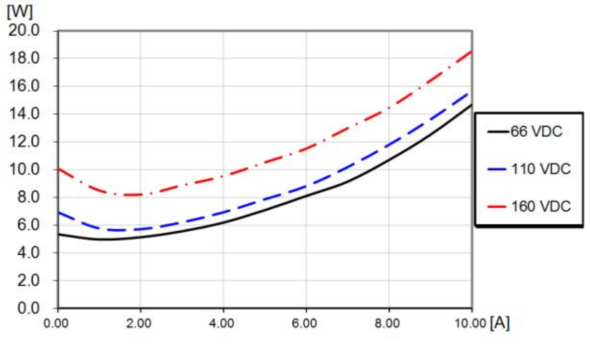
PKM 7215A PIP

Efficiency



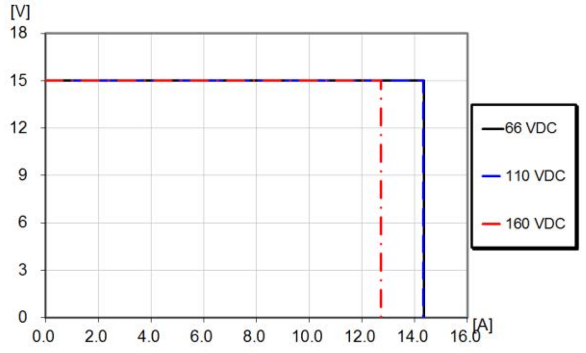
Efficiency vs. load current and input voltage at +25°C.

Power Dissipation



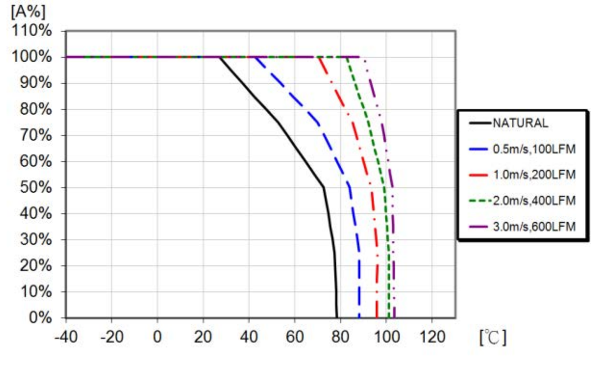
Dissipated power vs. load current and input voltage at +25°C.

Current Limit Characteristics



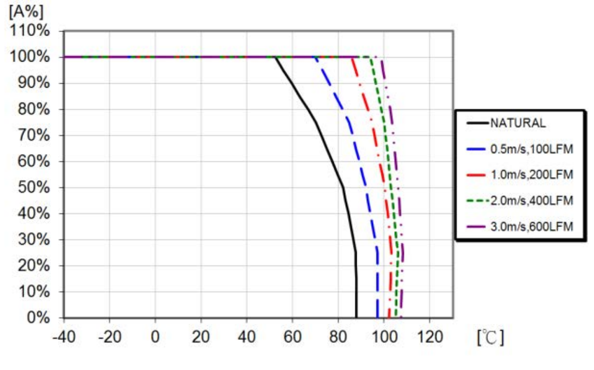
Output voltage vs. load current at I_O > max I_O at +25°C.

Output Current Derating(20mm ¼ brick heat sink)



Available load current vs. ambient air temperature and airflow at V_I=110 V. See Thermal Consideration section.

Output Current Derating(20mm ½ brick heat sink)

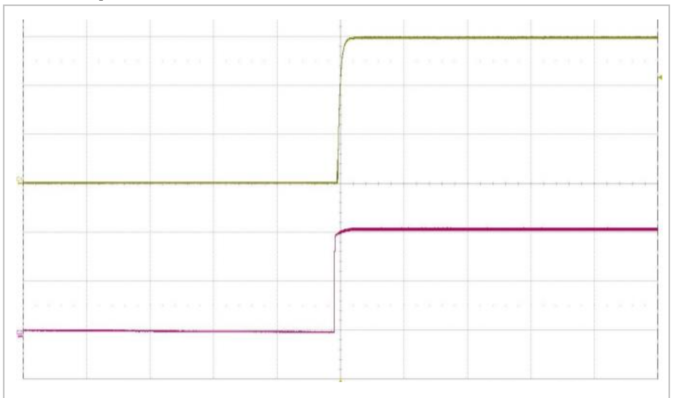


Available load current vs. ambient air temperature and airflow at V_I=110 V. See Thermal Consideration section.

Typical Characteristics
15 V, 10 A / 150 W

PKM 7215A PIP

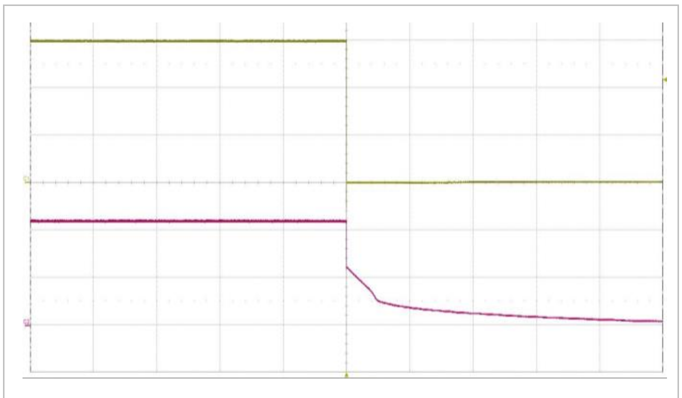
Start-up



Start-up enabled by connecting V_I at:
T_{P1} = +25°C, V_I =110 V,
I_O = 10 A resistive load.

Top trace: output voltage (5 V/div.).
Bottom trace: input voltage (50 V/div.).
Time scale: (100 ms/div.).

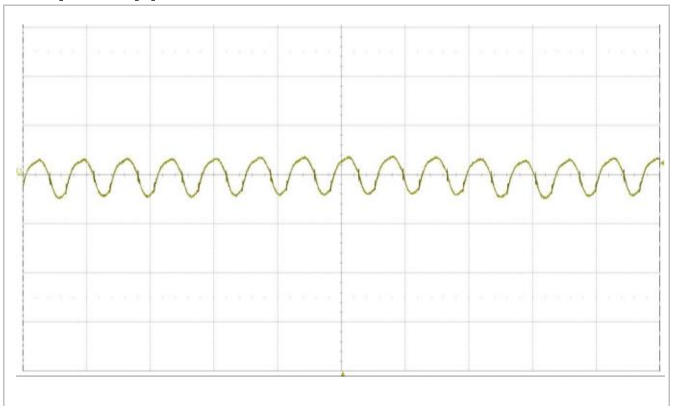
Shut-down



Shut-down enabled by disconnecting V_I at:
T_{P1} = +25°C, V_I = 110 V,
I_O = 10 A resistive load.

Top trace: output voltage (5 V/div.).
Bottom trace: input voltage (50 V/div.).
Time scale: (500 ms/div.).

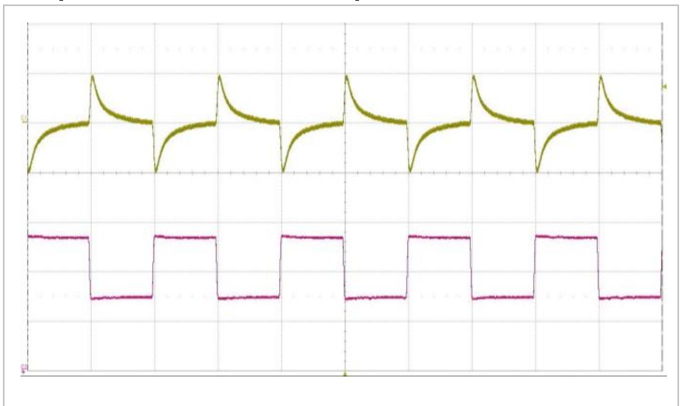
Output Ripple & Noise



Output voltage ripple at:
T_{P1} = +25°C, VI = 110 V,
I_O = 10 A resistive load.

Trace: output voltage (50 mV/div.).
Time scale: (5 µs/div.).

Output Load Transient Response



Output voltage response to load current step-change (5.0-7.5-5.0 A) at:
T_{P1} = +25°C, V_I = 110 V.

Top trace: output voltage (500 mV/div.).
Bottom trace: load current (2 A/div.).
Time scale: (1ms/div.).

Output Voltage Adjust (TRIM UP/TRIM DOWN)

The resistor value for an adjusted output voltage is calculated by using the following equations:
Output Voltage Adjust, Increase:

$$R_{\text{TRIM\_UP}} = \left(\frac{5.11 \times V_{o,set} \times (100 + \Delta\%)}{1.225 \times \Delta\%} - \frac{511}{\Delta\%} - 10.22 \right) \text{k}\Omega$$

$$\Delta\% = \left(\frac{V_{desired} - V_{o,set}}{V_{o,set}} \right) \times 100$$

Output Voltage Adjust, Decrease:

$$R_{\text{TRIM\_DOWN}} = \left(\frac{511}{\Delta\%} - 10.22 \right) \text{k}\Omega$$

$$\Delta\% = \left(\frac{V_{o,set} - V_{desired}}{V_{o,set}} \right) \times 100$$

Output Voltage=15V

Example:

To trim up the 15V model by 8% to 16.2V the required external resistor is:

$$R_{\text{TRIM\_UP}} = \left(\frac{5.11 \times 15 \times (100 + 8)}{1.225 \times 8} - \frac{511}{8} - 10.22 \right) = 770.62\ \text{k}\Omega$$

$$\Delta\% = \left(\frac{16.2 - 15}{15} \right) \times 100 = 8$$

Example:

To trim down the 15V model by 7% to 13.95V the required external resistor is:

$$R_{\text{TRIM\_DOWN}} = \left(\frac{511}{7} - 10.22 \right) = 62.78\ \text{k}\Omega$$

Electrical Specification
24 V, 6.25 A / 150 W

PKM 7216ZA PIP

T_{P1} = -40 to 105ºC, V_I = 66 to 160 V, {sense pins connected to output pins} unless otherwise specified under Conditions.
Typical values given at: T_{P1} = +25°C, V_I= 110 V_I max I_O, unless otherwise specified under Conditions.
Additional Cin = 47 µF, Cout = 10uF ceramic Cap. + 22uF E-Cap. See Operating Information section for selection of capacitor types.

Characteristics		Conditions	min	typ	max	Unit
V_I	Input voltage range		66		160	V
V_{Ioff}	Turn-off input voltage	Decreasing input voltage	60	62	64	V
V_{Ion}	Turn-on input voltage	Increasing input voltage	62	64	66	V
C_I	Internal input capacitance			47		µF
P_O	Output power		0		150	W
η	Efficiency	50% of max I_O		87		%
		max I_O		88		
		50% of max I_O, V_I = 110 V		87		
		max I_O, V_I = 110 V		88		
P_d	Power Dissipation	max I_O		20	23	W
P_{li}	Input idling power	I_O = 0 A, V_I = 110 V		1.0		W
P_{RC}	Input standby power	V_I = 110 V (turned off with RC)		0.7		W
f_s	Switching frequency	0-100 % of max I_O	297.5	350	402.5	kHz

V_{Oi}	Output voltage initial setting and accuracy	T_{P1} = +25°C, V_I = 110 V, I_O = 6.25 A	23.7	24	24.3	V
V_O	Output adjust range	See operating information	21.6	24	26.4	V
	Output voltage tolerance band	0-100% of max I_O	23.4		24.6	V
	Idling voltage	I_O = 0 A	23.4		24.6	V
	Line regulation	max I_O		12	120	mV
	Load regulation	V_I = 110 V, 25-100% of max I_O		25	240	mV
V_{tr}	Load transient voltage deviation	V_I = 110 V, Load step 50-75-50% of max I_O, di/dt = 100mA/µs		±400	±1000	mV
t_{tr}	Load transient recovery time			90	500	µs
t_r	Ramp-up time (from 10−90% of V_{Oi})	100% of max I_O			15	ms
t_s	Start-up time (from V_I connection to 90% of V_{Oi})				60	ms
t_{RC}	RC start-up time (from V_{RC} connection to 90% of V_{Oi})	max I_O		1.55	10	ms
RC	Sink current	See operating information	10			mA
	Trigger level	Decreasing / Increasing RC-voltage		0.8/2.5		V
I_O	Output current		0		6.25	A
I_{lim}	Current limit threshold	V_I = 110 V,T_{P1} < max T_{P1}		10	12	A
I_{sc}	Short circuit current	T_{P1} = 25ºC, see Note 1		0.05	0.1	A
C_{out}	Recommended Capacitive Load	T_{P1} = 25ºC, see Note 2	0		2000	µF
V_{Oac}	Output ripple & noise	See ripple & noise section, V_{Oi}		27	500	mVp-p
OVP	Over voltage protection	T_{P1} = +25°C, V_I = 110 V, 0-100% of max I_O		28		V

Note 1: hiccup mode

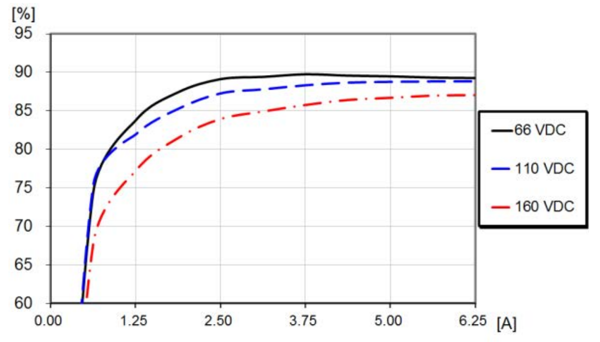
Note 2: Test condition: Electronic Capacitor and full load

PKM 7000A series Direct Converters Input 66-160 V, Output up to 12.5 A / 150 W	28701-BMR 711 Rev. B October 2017 © Flex

Typical Characteristics
24 V, 6.25 A / 150 W

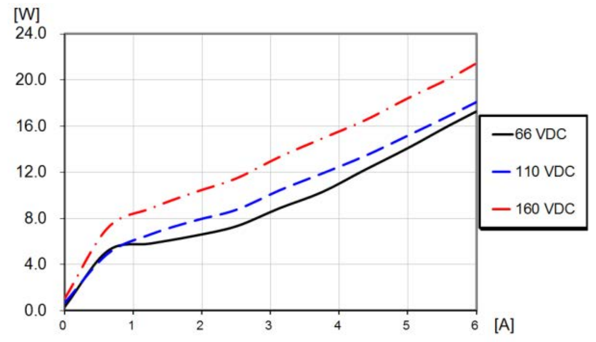
PKM 7216ZA PIP

Efficiency



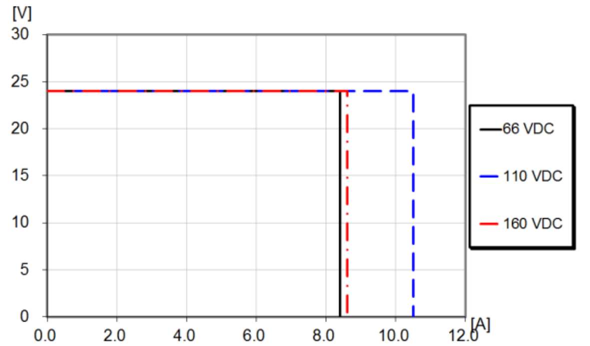
Efficiency vs. load current and input voltage at +25°C.

Power Dissipation



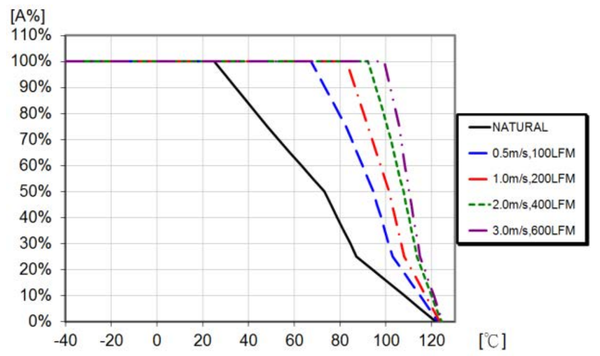
Dissipated power vs. load current and input voltage at +25°C.

Current Limit Characteristics



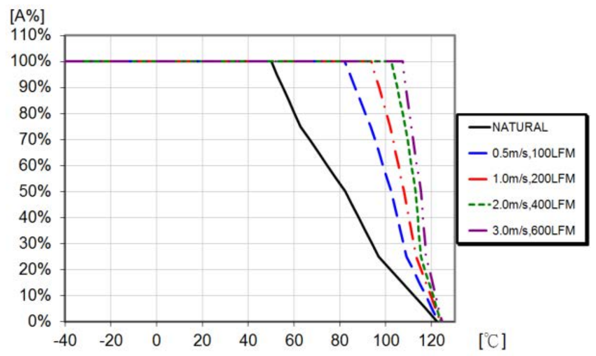
Output voltage vs. load current at I_O > max I_O at +25°C.

Output Current Derating(20mm ¼ brick heat sink)



Available load current vs. ambient air temperature and airflow at V_I=110 V. See Thermal Consideration section.

Output Current Derating(20mm ½ brick heat sink)

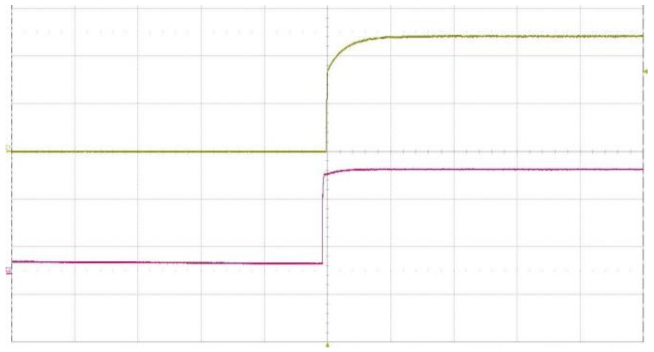


Available load current vs. ambient air temperature and airflow at V_I=110 V. See Thermal Consideration section.

Typical Characteristics
24 V, 6.25 A / 150 W

PKM 7216ZA PIP

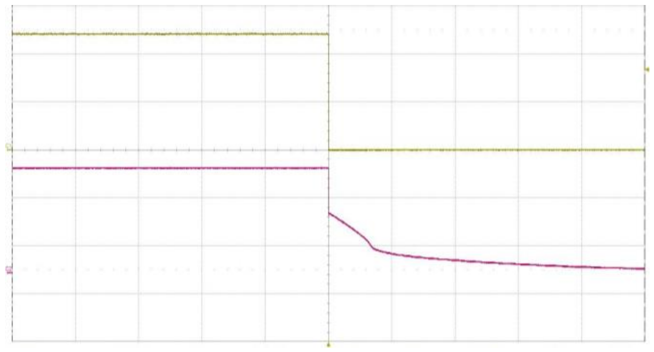
Start-up



Start-up enabled by connecting V_I at:
T_{P1} = +25°C, V_I =110 V,
I_O = 6.25 A resistive load.

Top trace: output voltage (10 V/div.).
Bottom trace: input voltage (50 V/div.).
Time scale: (50 ms/div.).

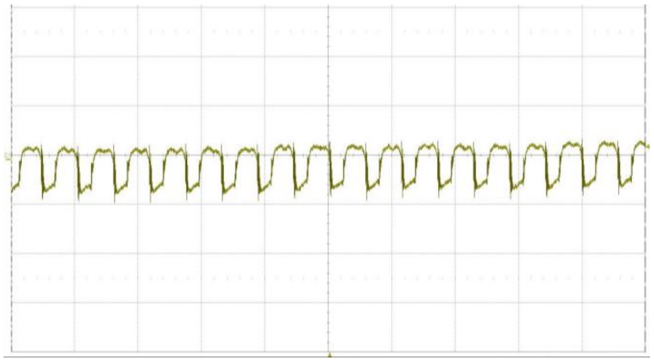
Shut-down



Shut-down enabled by disconnecting V_I at:
T_{P1} = +25°C, V_I = 110 V,
I_O = 6.25A resistive load.

Top trace: output voltage (10 V/div.).
Bottom trace: input voltage (50 V/div.).
Time scale: (500 ms/div.).

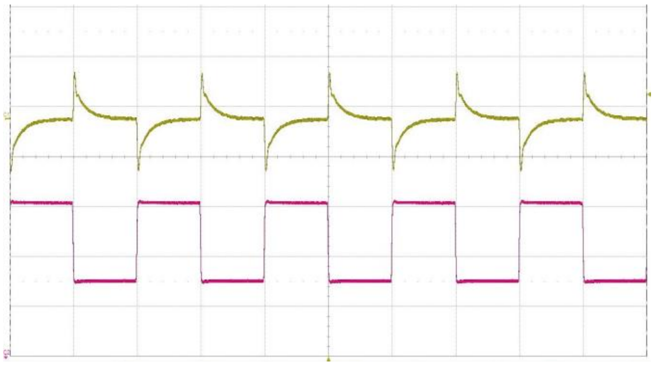
Output Ripple & Noise



Output voltage ripple at:
T_{P1} = +25°C, VI = 110 V,
I_O = 6.25 A resistive load.

Trace: output voltage (20 mV/div.).
Time scale: (5 µs/div.).

Output Load Transient Response



Output voltage response to load current step-change (3.125-4.68-3.125 A) at:
T_{P1} = +25°C, V_I = 110 V.

Top trace: output voltage (500 mV/div.).
Bottom trace: load current (1 A/div.).
Time scale: (1ms/div.).

Output Voltage Adjust (see operating information)

Output Voltage=24V

The resistor value for an adjusted output voltage is calculated by using the following equations:

Output Voltage Adjust, Increase:

$$R_{ADJ\_UP} = \left(\frac{14.6061}{\Delta} - 120 \right) \text{ k}\Omega$$

Output Voltage Adjust, Decrease:

$$R_{ADJ\_DOWN} = \left(\frac{17.2133}{\Delta} - 151.819 \right) \text{ k}\Omega$$

Example:

To trim up the 24V model by 8% to 25.92V the required external resistor is:

$$R_{ADJ\_UP} = \left(\frac{14.6061}{0.08} - 120 \right) = 62.58 \text{ k}\Omega$$

Example:

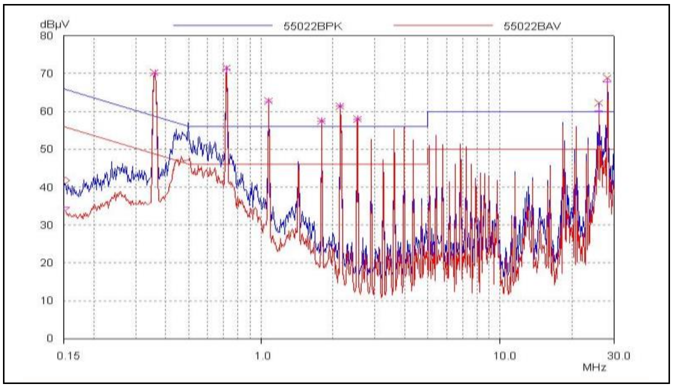
To trim down the 24V model by 7% to 22.32V the required external resistor is:

$$R_{ADJ\_DOWN} = \left(\frac{17.2133}{0.07} - 151.819 \right) = 94.08 \text{ k}\Omega$$

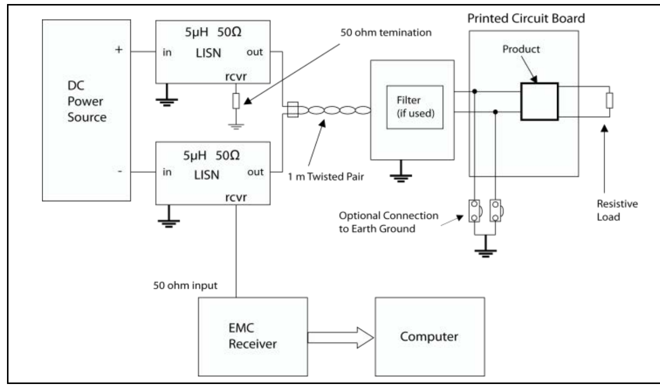
EMC Specification

Conducted EMI measured according to EN55022, CISPR 22 and FCC part 15J (see test set-up). See Design Note 009 for further information. The fundamental switching frequency is 350 kHz for PKM 7116ZA (100W/24V) at V_I = 110 V and max I_O.

Conducted EMI Input terminal value (typ)



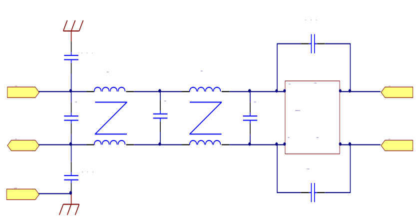
EMI without filter



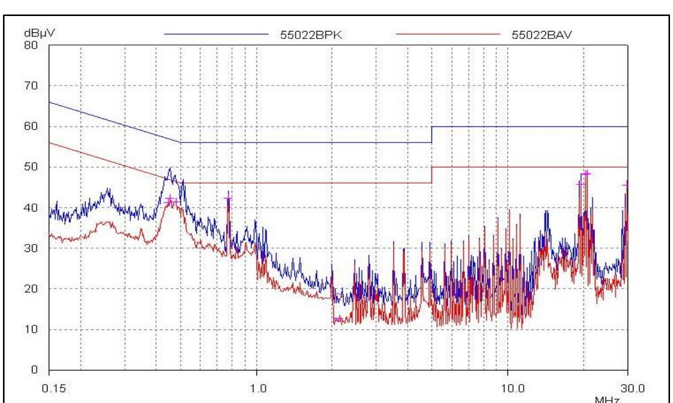
Test set-up

Optional external filter for class B

Suggested external input filter in order to meet class B in EN 55022, CISPR 22 and FCC part 15J.



Filter components:
C01,C02,C03 = 10uF
CY1,CY2,CY9,CY10 = 10KPF
L01 = 8mH
L02 = 45mH



EMI with filter

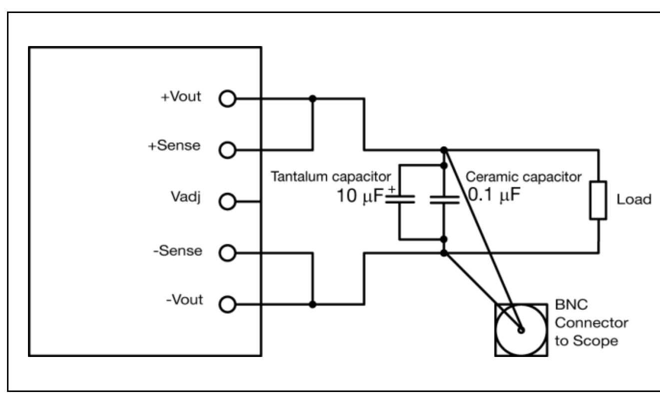
Layout recommendations

The radiated EMI performance of the product will depend on the PWB layout and ground layer design. It is also important to consider the stand-off of the product. If a ground layer is used, it should be connected to the output of the product and the equipment ground or chassis.

A ground layer will increase the stray capacitance in the PWB and improve the high frequency EMC performance.

Output ripple and noise

Output ripple and noise measured according to figure below. See Design Note 022 for detailed information.



Output ripple and noise test setup

Operating information

Input Voltage

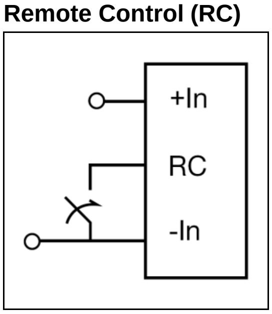
The input voltage range 66 to 160 Vdc meets the railway systems. At input voltages exceeding 160 V, the power loss will be higher than at normal input voltage and T_{P1} must be limited to absolute max 115°C. The absolute maximum continuous input voltage is 200 Vdc.

Short duration transient disturbances can occur on the DC distribution and input of the product when a short circuit fault occurs on the equipment side of a protective device (fuse or circuit breaker). The voltage level, duration and energy of the disturbance are dependant on the particular DC distribution network characteristics and can be sufficient to damage the product unless measures are taken to suppress or absorb this energy. The transient voltage can be limited by capacitors and other energy absorbing devices like zener diodes connected across the positive and negative input conductors at a number of strategic points in the distribution network. The end-user must secure that the transient voltage will not exceed the value stated in the Absolute maximum ratings. ETSI TR 100 283 examines the parameters of DC distribution networks and provides guidelines for controlling the transient and reduce its harmful effect.

Turn-off Input Voltage

The products monitor the input voltage and will turn on and turn off at predetermined levels.
The minimum hysteresis between turn on and turn off input voltage is 1.7 V.

Remote Control (RC)



The products are fitted with a remote control function referenced to the primary negative input Econnection (-In), with negative and positive logic options available.The RC function allows the product to be turned on/off by an external device like a semiconductor or mechanical switch. The RC pin has an internal pull up resistor to +In.
The external device must provide a minimum required sink current to guarantee a voltage not higher than maximum voltage on the RC pin (see Electrical characteristics table). When the RC pin is left open, the voltage generated on the RC pin is 3 - 5 V.

The standard product is provided with “negative logic” RC and will be on until the RC pin is connected to the -In. To turn off the product the RC pin should be left open, or connected to a voltage higher than 2 V referenced to -In. In situations where it is desired to have the product to power up automatically without the need for control signals or a switch, the RC pin can be wired directly to -In.

The second option is “positive logic” remote control, which can be ordered by adding the suffix “P” to the end of the part number. When the RC pin is left open, the product starts up automatically when the input voltage is applied. Turn off is achieved by connecting the RC pin to the -In. The product will restart automatically when this connection is opened.

See Design Note 021 for detailed information.

Input and Output Impedance

The impedance of both the input source and the load will interact with the impedance of the product. It is important that the input source has low characteristic impedance. The products are designed for stable operation without external capacitors connected to the input or output. The performance in some applications can be enhanced by addition of external capacitance as described under External Decoupling Capacitors.

If the input voltage source contains significant inductance, the addition of a 22 - 100 µF capacitor across the input of the product will ensure stable operation. The capacitor is not required when powering the product from an input source with an inductance below 10 µH. The minimum required capacitance value depends on the output power and the input voltage. The higher output power the higher input capacitance is needed.

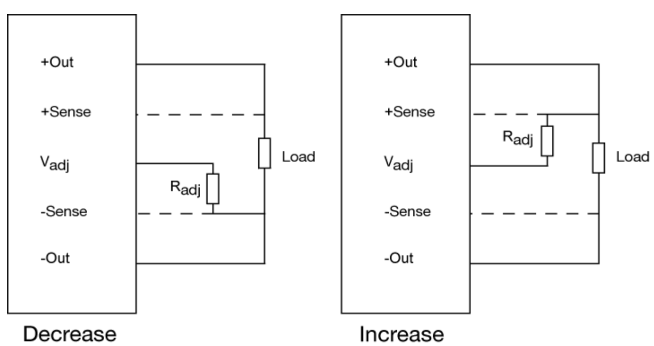
External Decoupling Capacitors

When powering loads with significant dynamic current requirements, the voltage regulation at the point of load can be improved by addition of decoupling capacitors at the load. The most effective technique is to locate low ESR ceramic and electrolytic capacitors as close to the load as possible, using several parallel capacitors to lower the effective ESR. The ceramic capacitors will handle high-frequency dynamic load changes while the electrolytic capacitors are used to handle low frequency dynamic load changes. It is equally important to use low resistance and low inductance PWB layouts and cabling.

External decoupling capacitors will become part of the product’s control loop. The control loop is optimized for a wide range of external capacitance and the maximum recommended value that could be used without any additional analysis is found in the Electrical specification.
The ESR of the capacitors is a very important parameter. Stable operation is guaranteed with a verified ESR value of >5 mΩ across the output connections.
For further information please contact your local Flex representative.

Output Voltage Adjust (V_{adj})

The products have an Output Voltage Adjust pin (V_{adj}). This pin can be used to adjust the output voltage above or below Output voltage initial setting.
When increasing the output voltage, the voltage at the output pins (including any remote sense compensation) must be kept below the threshold of the over voltage protection, (OVP) to prevent the product from shutting down. At increased output voltages the maximum power rating of the product remains the same, and the max output current must be decreased correspondingly.
To increase the voltage the resistor should be connected between the V_{adj} pin and +Sense pin. The resistor value of the Output voltage adjust function is according to information given under the Output section for the respective product.
To decrease the output voltage, the resistor should be connected between the V_{adj} pin and –Sense pin.



Parallel Operation

Two products may be paralleled for redundancy if the total power is equal or less than P_O max. It is not recommended to parallel the products without using external current sharing circuits.

See Design Note 006 for detailed information.

Remote Sense

The products have remote sense that can be used to compensate for voltage drops between the output and the point of load. The sense traces should be located close to the PWB ground layer to reduce noise susceptibility. The remote sense circuitry will compensate for up to 10% voltage drop between output pins and the point of load.

If the remote sense is not needed +Sense should be connected to +Out and -Sense should be connected to -Out.

Over Temperature Protection (OTP)

The products are protected from thermal overload by an internal over temperature shutdown circuit.
When T_{P1} as defined in thermal consideration section exceeds 115°C the product will shut down. The product will make continuous attempts to start up (non-latching mode) and resume normal operation automatically when the temperature has dropped >10°C below the temperature threshold.

Over Voltage Protection (OVP)

The products have output over voltage protection that will shut down the product in over voltage conditions. The product will make continuous attempts to start up (non-latching mode) and resume normal operation automatically after removal of the over voltage condition.

Over Current Protection (OCP)

The products include current limiting circuitry for protection at continuous overload. The output voltage will decrease towards zero for output currents in excess of max output current (max I_O). The product will resume normal operation after removal of the overload. The load distribution should be designed for the maximum output short circuit current specified.

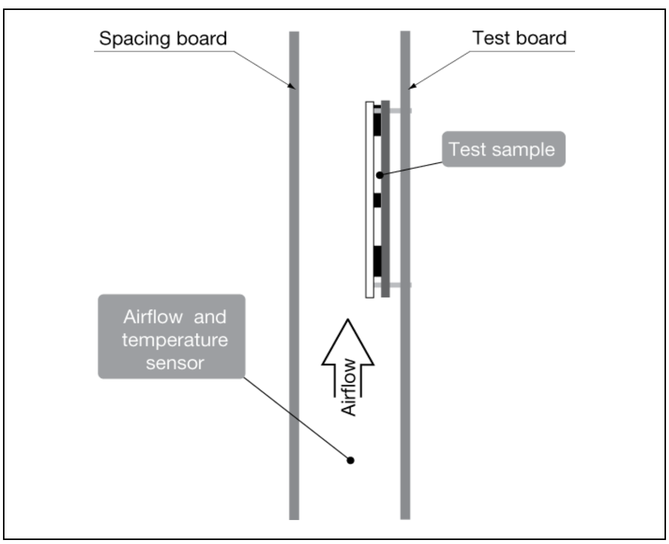
Thermal Consideration

General

The products are designed to operate in different thermal environments and sufficient cooling must be provided to ensure reliable operation.

For products mounted on a PWB without a heat sink attached, cooling is achieved mainly by conduction, from the pins to the host board, and convection, which is dependent on the airflow across the product. Increased airflow enhances the cooling of the product. The Output Current Derating graph found in the Output section for each model provides the available output current vs. ambient air temperature and air velocity at V_I = 110V.

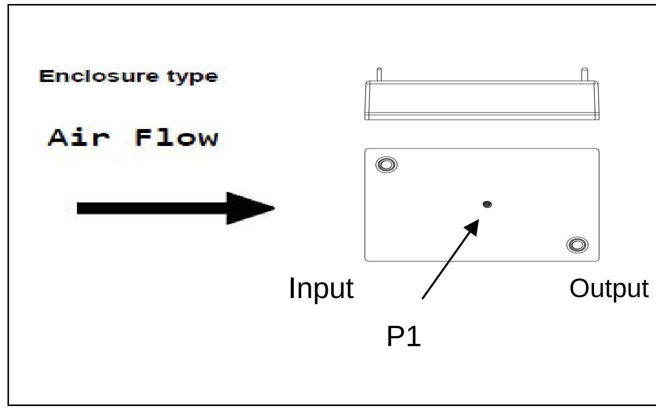
The product is tested on a 254 x 254 mm, 35 µm (1 oz), 8-layer test board mounted vertically in a wind tunnel with a cross-section of 608 x 203 mm.



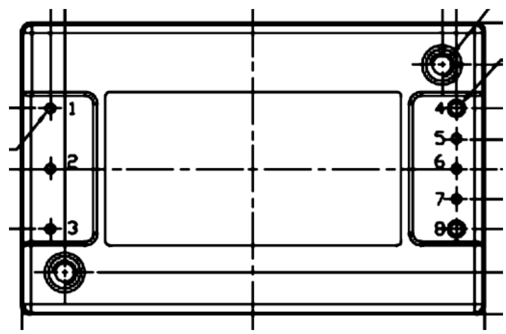
Definition of product operating temperature

The product operating temperatures is used to monitor the temperature of the product, and proper thermal conditions can be verified by measuring the temperature at positions P1. The temperature at this position (T_{P1}) should not exceed the maximum temperatures in the table below. Temperature above maximum T_{P1}, measured at the reference point P1 are not allowed and may cause permanent damage.

Position	Description	Max Temp.
P1	Reference point	115°C

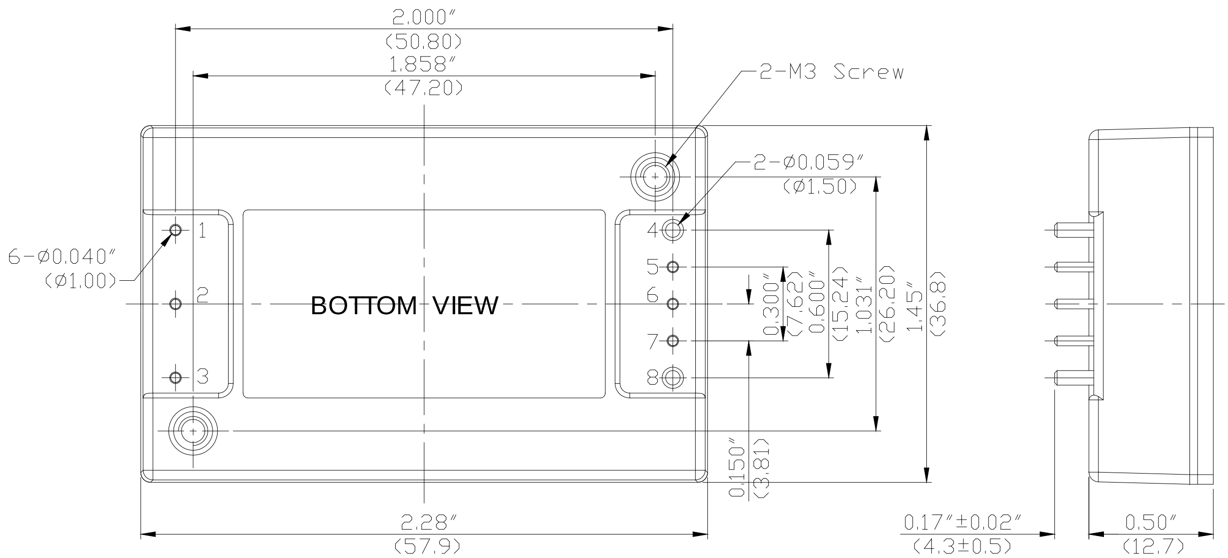


Connections



Pin Connections	
Pin	Function
1	-Vin
2	Remote On/Off Control
3	+Vin
4	-Vout
5	-Vsense
6	Trim
7	+Vsense
8	+Vout

Mechanical Information - Enclosure Type

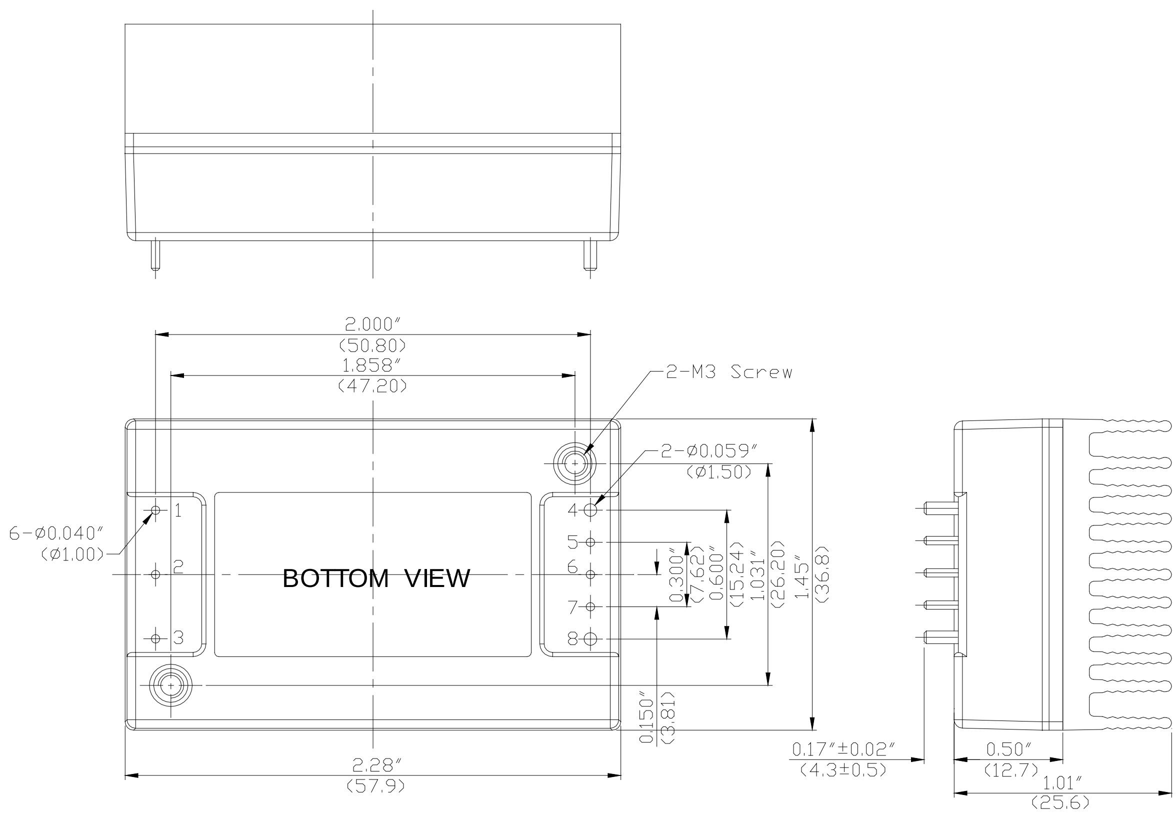


Pin Connections	
Pin	Function
1	-Vin
2	Remote On/Off Control
3	+Vin
4	-Vout
5	-Vsense
6	Trim
7	+Vsense
8	+Vout

Notes:
1.Pins:
Material: Brass
Plating: Nickel

2.Weight: typical 70g
All dimensions in inches (mm).
Tolerance .xx= ±0.04”
.xxx=±0.010”

Mechanical Information – 10mm ¼ brick heat sink

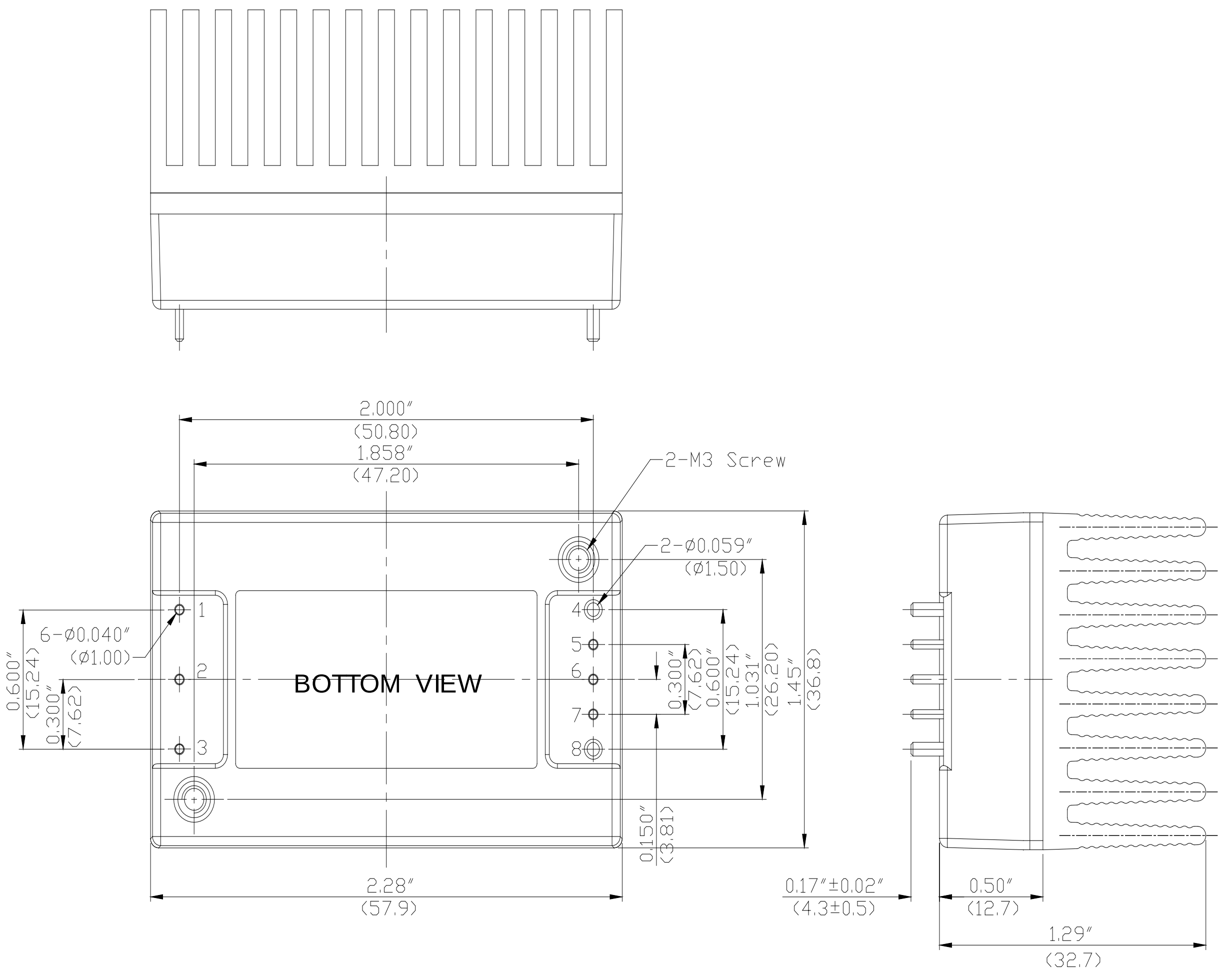


Notes:
1.Pins:
Material: Brass
Plating: Nickel

2.Heatsink:
Material: aluminum
Plating: Anodized

3.Weight: typical 110g
All dimensions in inches (mm).
Tolerance .xx= ±0.04”
.xxx=±0.010”

Mechanical Information - 20mm ¼ brick heat sink

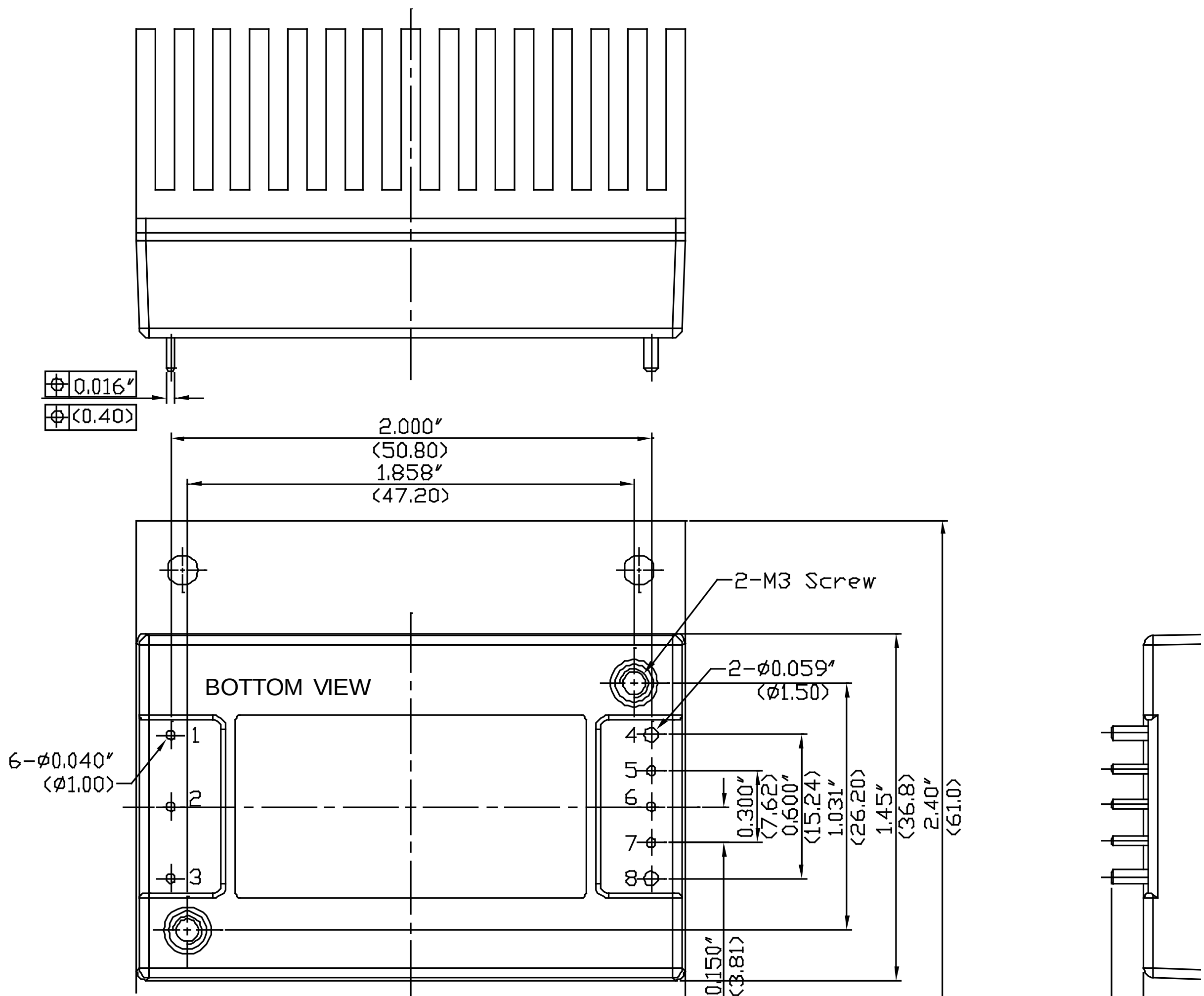


Notes:
1.Pins:
Material: Brass
Plating: Nickel

2.Heatsink:
Material: aluminum
Plating: Anodized

3.Weight: typical 130g
All dimensions in inches (mm).
Tolerance .xx= ±0.04”
.xxx=±0.010”

Mechanical Information - 20mm ½ brick heat sink



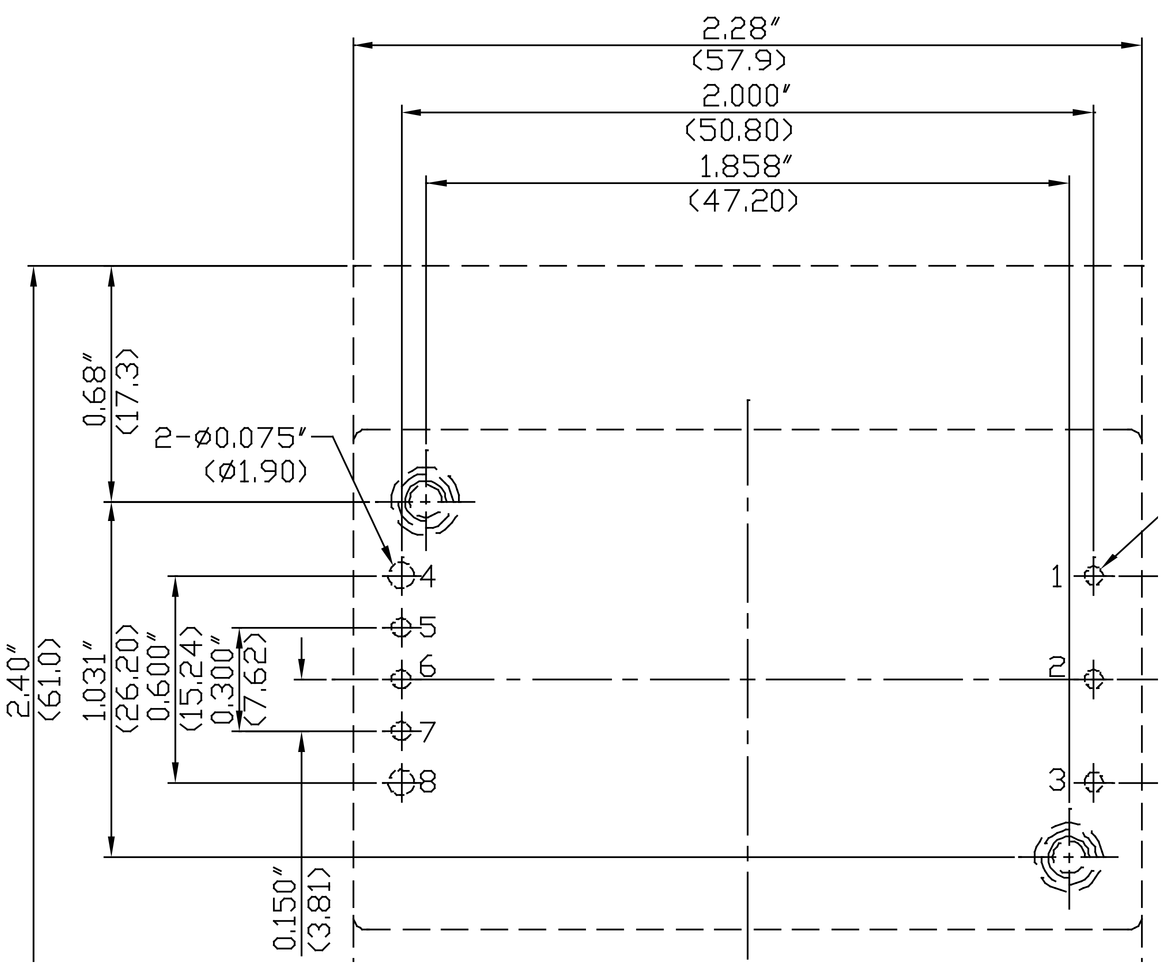
Notes:

1.Pins:
Material: Brass
Plating: Nickel

2.Heatsink:
Material: alumInum
Plating: Anodized

3.Weight : typical 140g
All dimensions in inches (mm).
Tolerance .xx= ±0.04"
.xxx=±0.010"

RECOMMENDED FOOTPRINT
TOP VIEW



Soldering Information - Hole Mounting

The hole mounted product is intended for plated through hole mounting by wave or manual soldering. The pin temperature is specified to maximum to 270°C for maximum 10 seconds.

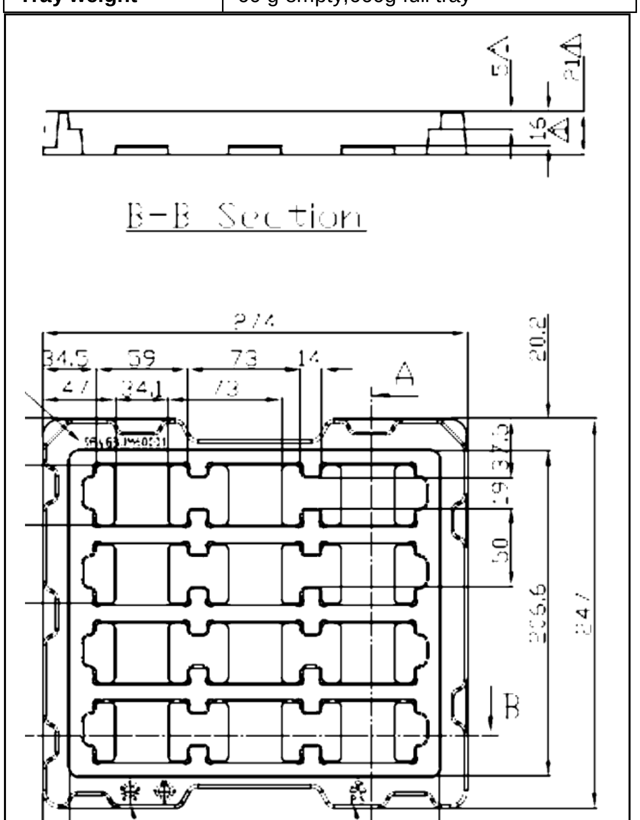
A maximum preheat rate of 4°C/s and maximum preheat temperature of 150°C is suggested. When soldering by hand, care should be taken to avoid direct contact between the hot soldering iron tip and the pins for more than a few seconds in order to prevent overheating.

A no-clean flux is recommended to avoid entrapment of cleaning fluids in cavities inside the product or between the product and the host board. The cleaning residues may affect long time reliability and isolation voltage.

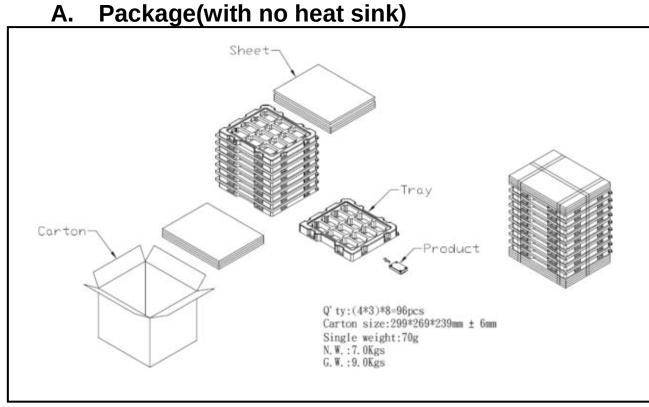
Delivery Package Information

The products are delivered in antistatic clamshell trays

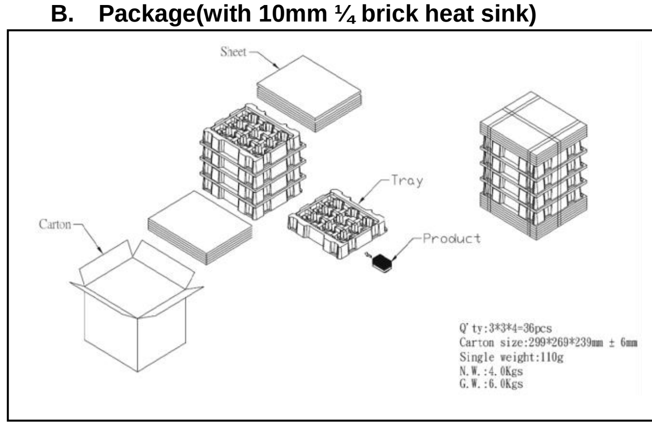
Tray Specifications	
Material	Antistatic PS
Surface resistance	10^5 < Ohm/square < 10^{11}
Bakability	This tray is not bake-able
Tray thickness	23.1 mm [0.9094 inch]
Box capacity	96 products (8 full trays/box)
Tray weight	60 g empty,660g full tray



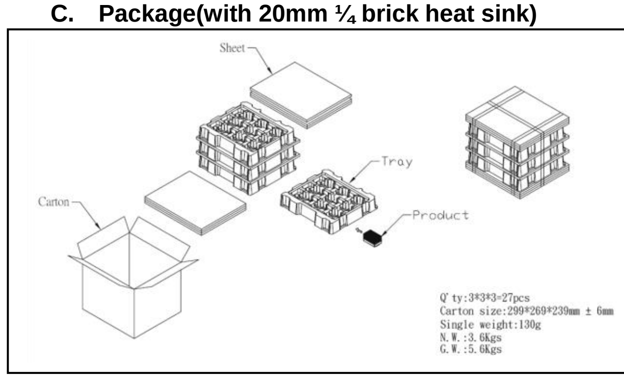
A. Package(with no heat sink)



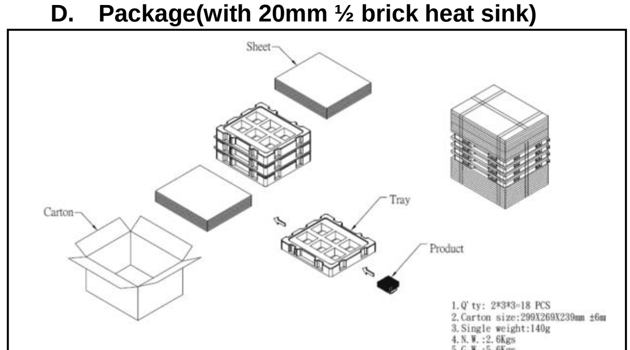
B. Package(with 10mm ¼ brick heat sink)



C. Package(with 20mm ¼ brick heat sink)



D. Package(with 20mm ½ brick heat sink)



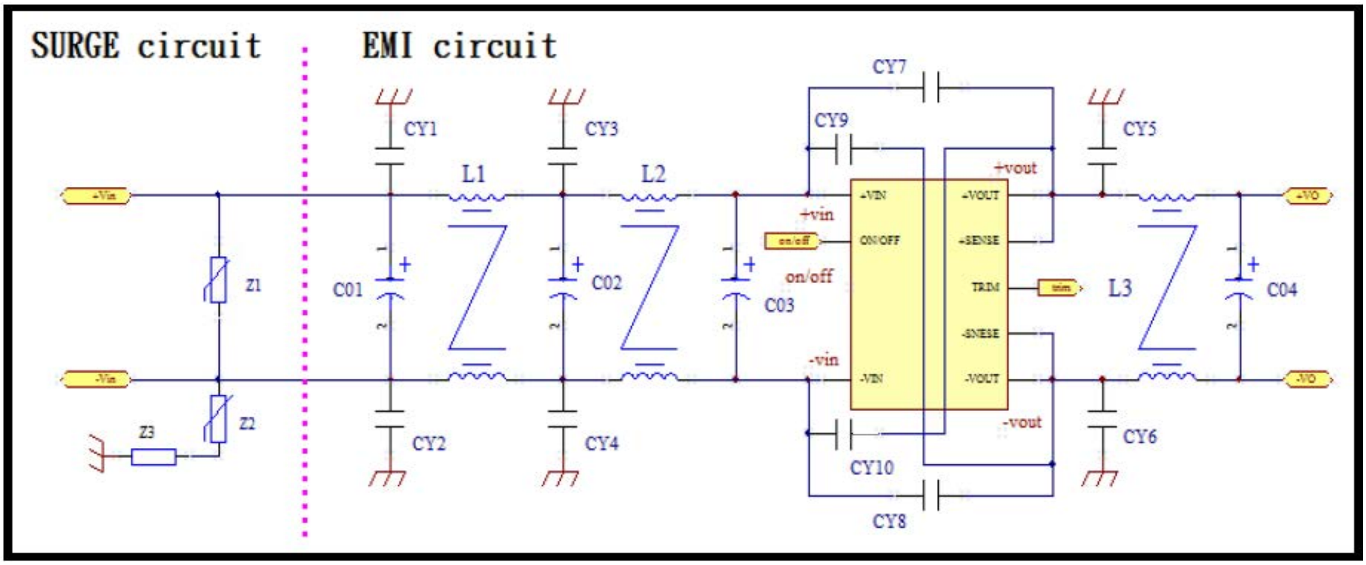
Product Qualification Specification

Characteristics			
External visual inspection	IPC-A-610		
Change of temperature (Temperature cycling)	IEC 60068-2-14 Na	Temperature range Number of cycles Dwell/transfer time	-55 to 105°C 20 30 min/3 min
Cold (in operation)	IEC 60068-2-1 Ad	Temperature T_A Duration	-45°C 72 h
Damp heat	IEC 60068-2-30	Temperature Humidity Duration	45°C 95 % RH 72 hours
Dry heat	IEC 60068-2-2 Bd	Temperature Duration	125°C 1000 h
Electrostatic discharge susceptibility	IEC 61340-3-1, JESD 22-A114	Human body model (HBM)	Class 2, 2000 V
Immersion in cleaning solvents	IEC 60068-2-45 XA, method 2	Water Glycol ether {Isopropyl alcohol}	55°C 35°C {35°C}
Mechanical shock	IEC 60068-2-27 Ea	Peak acceleration Duration	200 g 6 ms
Moisture reflow sensitivity [1]	J-STD-020E	Level 1 (SnPb-eutectic) Level 3 (Pb Free)	225°C 260°C
Operational life test	MIL-STD-202G, method 108A	Duration	1000 h
Resistance to soldering heat [2]	IEC 60068-2-20 Tb, method 1A	Solder temperature Duration	270°C 10-13 s
Robustness of terminations	IEC 60068-2-21 Test Ua1	Through hole mount products	All leads
Solderability	IEC 60068-2-20 test Ta [1]	Preconditioning Temperature, SnPb Eutectic Temperature, Pb-free	235°C 245°C
Vibration, broad band random	IEC 60068-2-64 Fh, method 1	Frequency Spectral density Duration	10 to 500 Hz 0.07 g^2/Hz 10 min in each direction

Notes
[1] Only for products intended for wave soldering (plated through hole products)

EN 50155		
Phenomenon	EN 50155 Reference Clause(s)	Reference Standard
Characteristic Test	12.2.1, 12.2.2, 5.1.1.1, 5.1.3, 12.2.9, 12.2.6	-
EMC	12.2.7, 12.2.8	EN 50121-3-2 EN 61000-4 EN 55011
Environmental Tests	12.2.3, 12.2.4, 12.2.5,12.2.11	EN 60068-2 EN 61373

Surge and EMI Circuit



MODEL	PKM7111A	PKM7113A	PKM7115A	PKM7116ZA	PKM7213A	PKM7215A	PKM7216ZA
Output	5V/20A	12V/8.3A	15V/6.67A	24V/4.16A	12V/12.5A	15V/10A	24V/6.25A
Surge Circuit							
Z1 and Z2 (Varistor)	180V	180V	180V	180V	180V	180V	180V
Z3 (Surge Protector):	3000V, 100A	3000V, 100A	3000V, 100A	3000V, 100A	3000V, 100A	3000V, 100A	3000V, 100A
EMI Circuit							
L1	385uH	4.6mH	385uH	385uH	4.6mH	385uH	385uH
L2	385uH	12mH	2mH	385uH	12mH	2mH	385uH
L3	5mH	576uH	Jump-wire	5mH	576uH	Jump-wire	5mH
C01	150uF	100uF	150uF	150uF	100uF	150uF	150uF
C02	22uF	100uF	22uF	22uF	100uF	22uF	22uF
C03	150uF	100uF	22uF	150uF	100uF	22uF	150uF
C04	390uF	470PF	390uF	390uF	470PF	390uF	390uF
CY1	1nF	4.7nF	1nF	1nF	4.7nF	1nF	1nF
CY2	1nF + 1pcs Bead Core	4.7nF	1nF	1nF + 1pcs Bead Core	4.7nF	1nF	1nF + 1pcs Bead Core
CY3	3.3nF	X	2.2nF	3.3nF	X	2.2nF	3.3nF
CY4	3.3nF	2.2nF	2.2nF	3.3nF	2.2nF	2.2nF	3.3nF
CY5	2.2nF+2pcs Bead Core	4.7nF	2.2nF	2.2nF+2pcs Bead Core	4.7nF	2.2nF	2.2nF+2pcs Bead Core
CY6	2.2nF+1pcs Bead Core	X	2.2nF	2.2nF+2pcs Bead Core	X	2.2nF	2.2nF+1pcs Bead Core
CY7 & CY8	2.2nF	4.7nF	4.7nF+2pcs Bead Core	2.2nF	4.7nF	4.7nF+2pcs Bead Core	2.2nF
CY9 &CY10	150p	X	X	150p	X	X	150p

Note X : No Component